

**DISPLAY Elektronik GmbH**

# DATA SHEET

**LCD MODULE**

**DEM 480272E VMX-PW-N  
(C-TOUCH)**

4,3" TFT with CTP

*Product Specification*

*Version: 0*

**02.02.2026**

# GENERAL SPECIFICATION

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MODULE NO. :

**DEM 480272E VMX-PW-N  
(C-TOUCH)**

CUSTOMER

VERSION NO.	CHANGE DESCRIPTION	DATE
0	Original Version	02.02.2026

PREPARED BY: CC

DATE: 02.02.2026

APPROVED BY: MH

DATE: 02.02.2026

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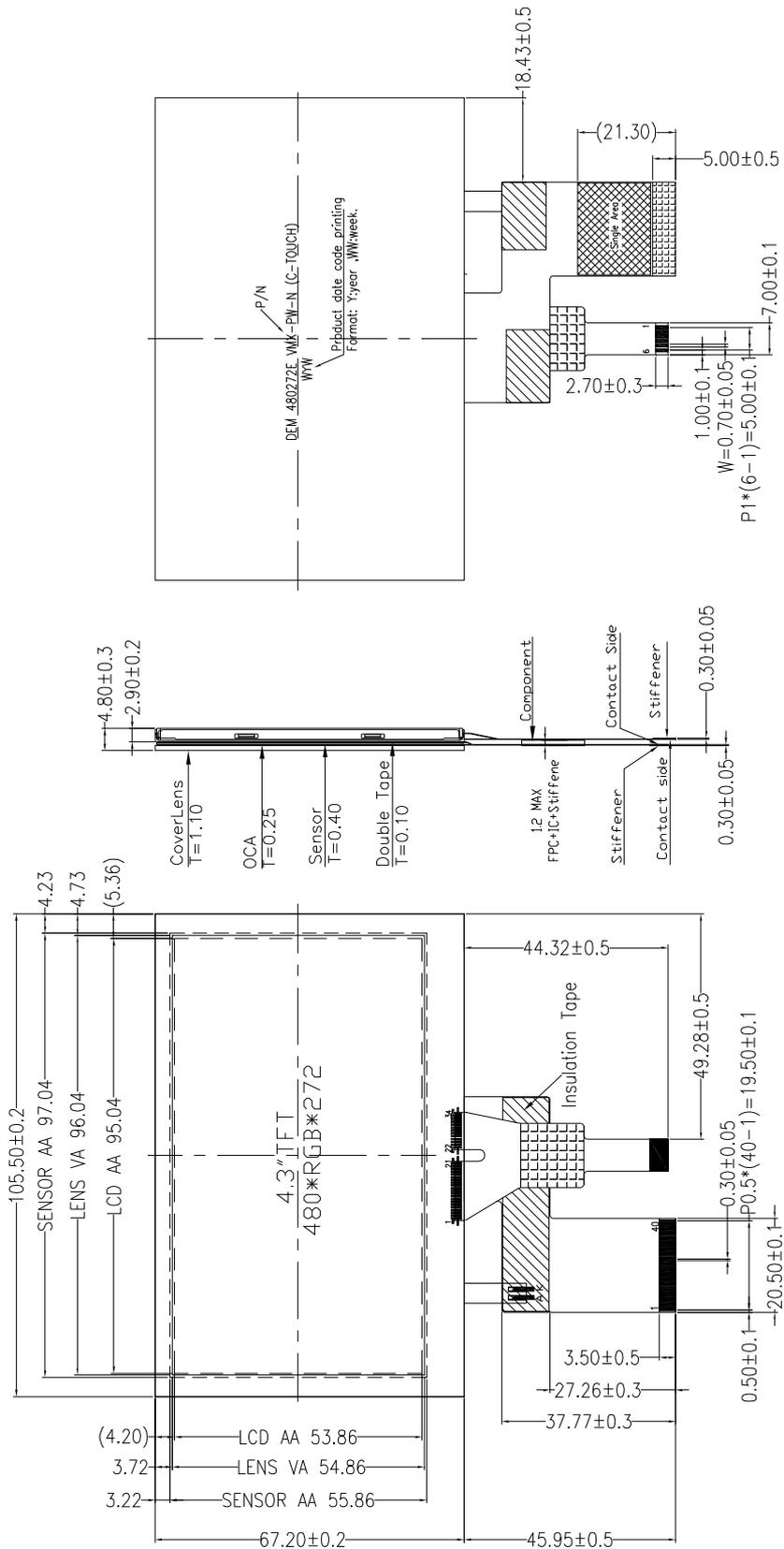
**1. GENERAL SPECIFICATIONS**

<b>ITEM</b>	<b>STANDARD VALUE</b>	<b>UNIT</b>
LCD TYPE	TFT, IPS, NORMALLY BLACK, TRANSMISSIVE, NEGATIVE	-
MODULE SIZE	105.50 x 67.20 x 4.80	mm
ACTIVE AREA	95.04 x 53.86	mm
PIXEL PITCH	0.198 x 0.198	mm
NUMBER OF DOTS	480 x RGB x 272	-
DRIVER IC	SC7283-G4, Sitronix	-
INTERFACE TYPE	24/18-BIT-RGB	-
RECOMMENDED VIEWING DIRECTION	ALL	-
COLOURS	16.7 MILLION	-
BACKLIGHT TYPE	7-CHIP WHITE LED	-
TOUCH PANEL TYPE	CTP	-

**Touch Panel:**

<b>ITEM</b>	<b>CONTENTS</b>
Type	Capacitive Multi-Touch
Module Size	105.50 x 67.20 x 1.90 mm
Viewing Area	96.04 x 54.86 mm
Driver IC	FT5426G, Focaltech
Interface	I2C
Operating Voltage	2.8V ~ 3.6 V
ITO Glass Thickness	0.4mm
Operating Humidity	20% ~ 90% RH
Storage Humidity	20% ~ 90% RH
Resolution	480 x 272
Input Force	≤ 10g
Operating temperature	-30°C to +85°C
Storage temperature	-30°C to +85°C
Input Method	Finger or exclusive pen
Transparency	≥ 88%
Surface Hardness	> 6H
Hitting Life	35.000.000 times
Insulation	20M Ohm or over (DC 25V)
ESD (Human Body Model)	2.000 V

2. EXTERNAL DIMENSIONS



Circuit diagram:



7 PCS WHITE LED

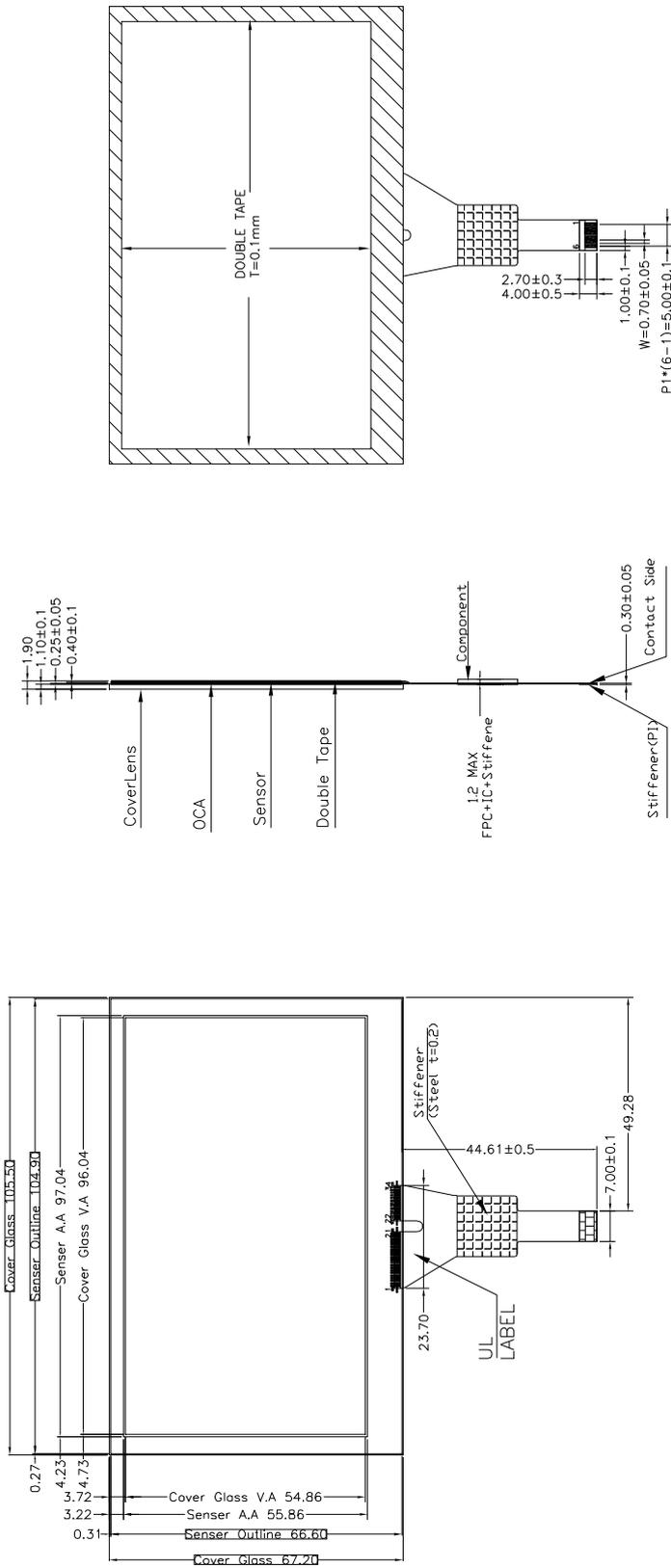
20mA@ 22.4(TYP)V

Brightness 280cd/m<sup>2</sup>(TYP)

Remark:

- 1.Unmarked tolerance is ±0.2
- 2.All materials comply with RoHS
3.  ...:critical dimension.

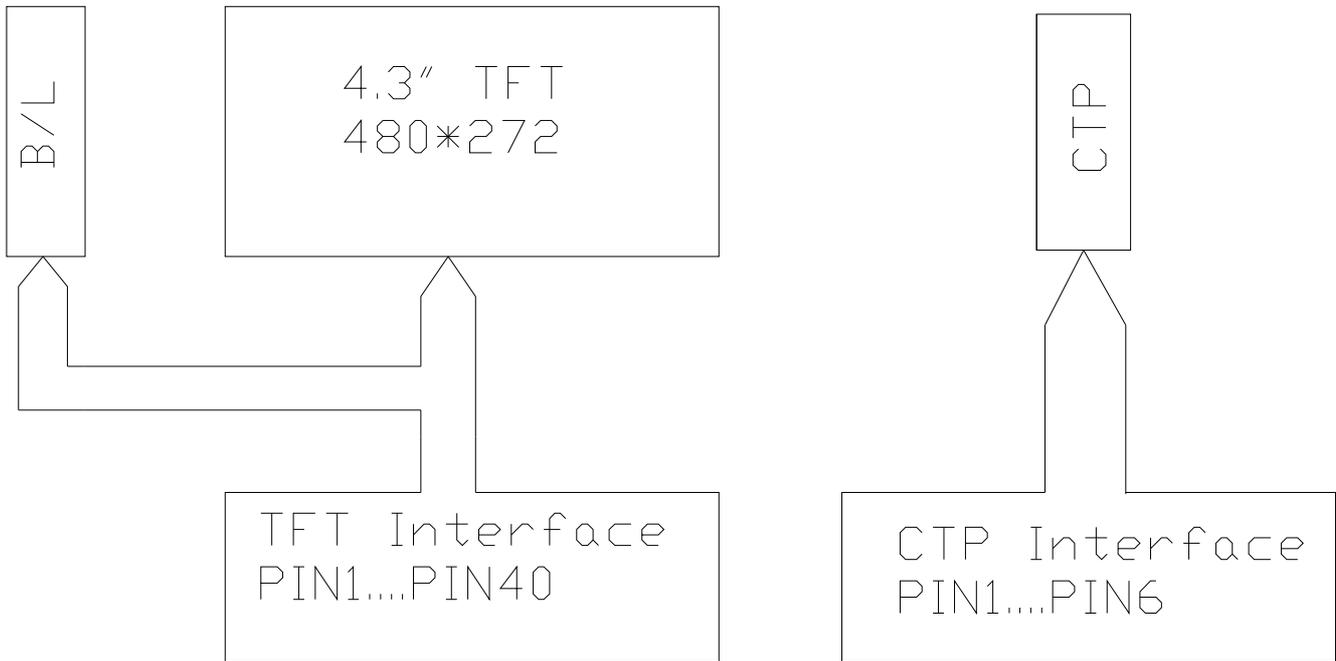
Touch Panel:



Remark:

1. Unmarked tolerance is ±0.3
2. All materials comply with RoHS

**3. BLOCK DIAGRAM**



## 4. PIN ASSIGNMENT

No.	Symbol	Function
1	VLED-	Backlight Cathode
2	VLED+	Backlight Anode
3	GND	Ground
4	VDD	Power source
5	R0	Red data signal
6	R1	Red data signal
7	R2	Red data signal
8	R3	Red data signal
9	R4	Red data signal
10	R5	Red data signal
11	R6	Red data signal
12	R7	Red data signal
13	G0	Green data signal
14	G1	Green data signal
15	G2	Green data signal
16	G3	Green data signal
17	G4	Green data signal
18	G5	Green data signal
19	G6	Green data signal
20	G7	Green data signal
21	B0	Blue data signal
22	B1	Blue data signal
23	B2	Blue data signal
24	B3	Blue data signal
25	B4	Blue data signal
26	B5	Blue data signal
27	B6	Blue data signal
28	B7	Blue data signal
29	GND	Ground
30	PCLK	Clock signal to sample each data
31	DISP	Display on/off signal DISP="H" Display on DISP="L"
32	HSYNC	Horizontal synchronizing signal
33	VSYNC	Vertical synchronizing signal
34	DE	Input data enable control.
35	NC	No connection
36	GND	Ground
37	NC(XR)	No connection
38	NC(YD)	No connection
39	NC(XL)	No connection
40	NC(YU)	No connection

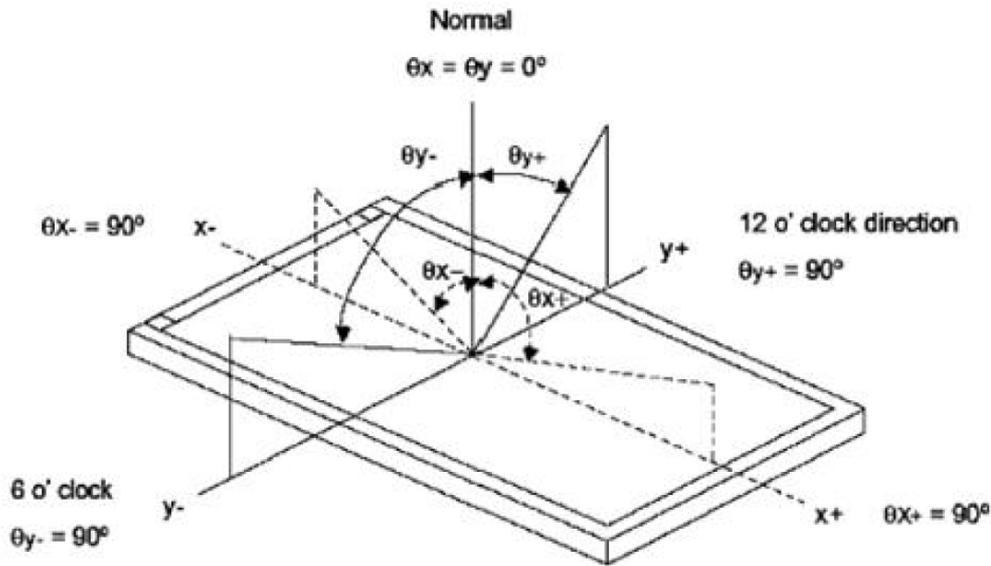
**Touch Panel:**

Pin No.	Symbol	Description
1	VCC	Positive power supply(3.3V)
2	GND	Negative supply
3	I2C_SCL	I2C Serial clock input/output
4	I2C_SDA	I2C Serial data input/output
5	INT	External Interrupt
6	RST	External Reset, Low is active

**5. OPTICAL CHARACTERISTICS**

ITEM	SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE	
			MIN	TYP.	MAX			
Luminance	L	I <sub>L</sub> =40mA	-	280	-	Cd/m <sup>2</sup>	-	
Contrast Ratio	CR	θ =0° VIEWING NORMAL ANGLE	640	800	-	-	-	
Response Time	T <sub>on</sub> + T <sub>off</sub>		-	30	40	ms	-	
CIE COLOUR COORDINATE	RED		XR	-	0.629	-	-	-
			YR	-	0.326	-	-	-
	GREEN		XG	-	0.337	-	-	-
			YG	-	0.546	-	-	-
	BLUE		XB	-	0.136	-	-	-
			YB	-	0.143	-	-	-
	WHITE		XW	-	0.320	-	-	-
			YW	-	0.345	-	-	-
VIEWING ANGLE	Hor.	θ x+	70	80	-	Degree	-	
		θ x-	70	80	-		-	
	Ver.	θ y+	70	80	-		-	
		θ y-	70	80	-		-	

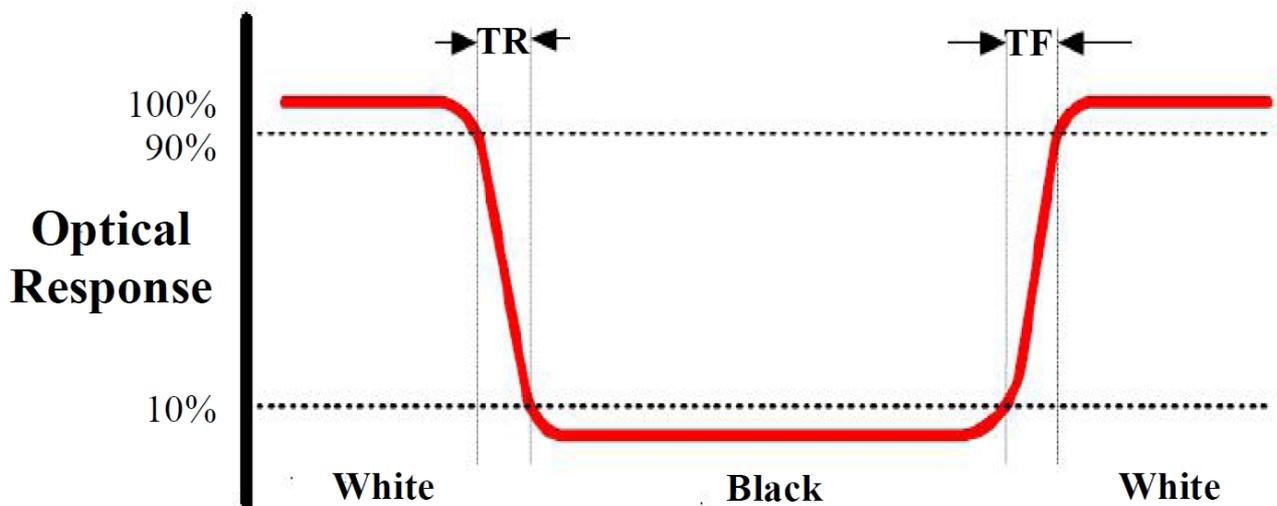
**Note 1: Definition of Viewing Angle  $\theta_x$  and  $\theta_y$ :**



**Note 2: Definition of contrast ratio CR:**

$$CR = \frac{\text{Luminance of white state}}{\text{Luminance of black state}}$$

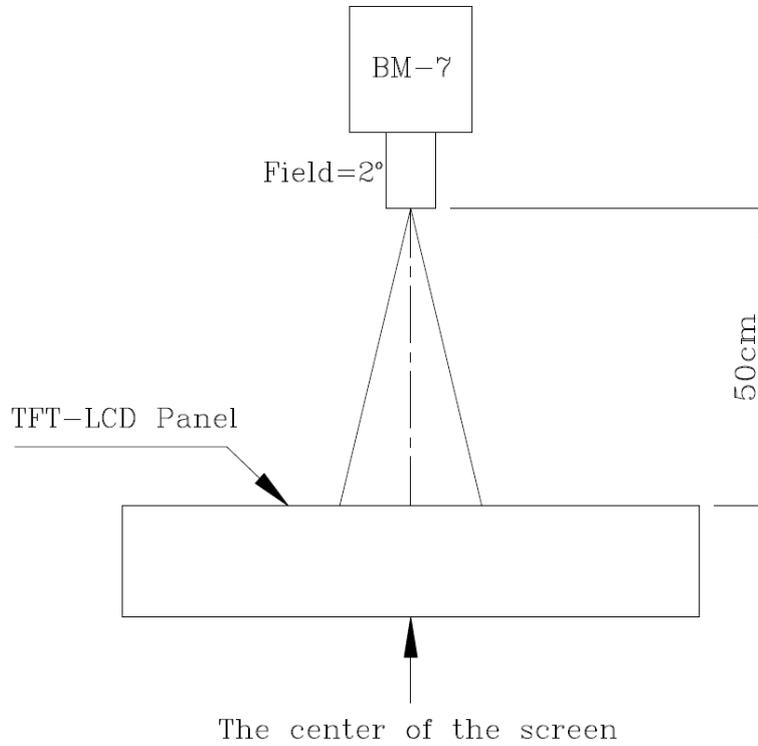
**Note 3: Definition of Response Time ( $T_r, T_f$ )**



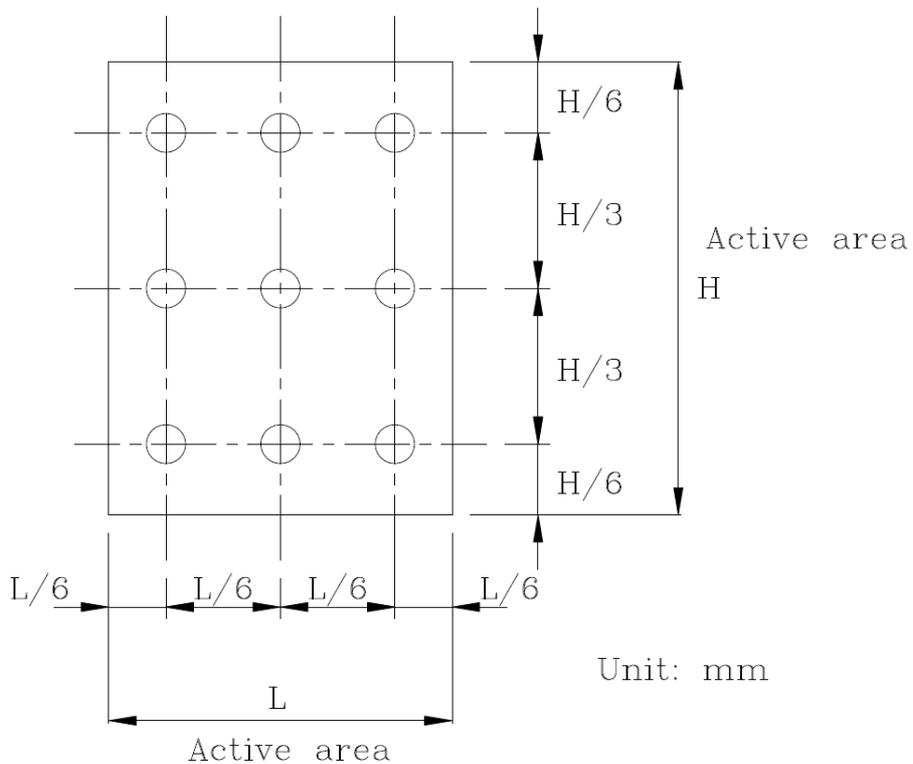
**Note 4: Definition of Luminance**

**① The Brightness Test Equipment Setup**

Field=2° (As measuring “black” image, field=2° is the best testing condition)



**② The Brightness Test Point Setup**



**6. ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	MIN	MAX	UNIT
Power Supply Voltage	VDD	-0.3	4.0	V
IO Supply Voltage	VDDI	-0.3	4.0	V
Supply Current (one LED)	I(LED)	-	30	mA
Operating Temperature	Top	-30	+85	°C
Storage Temperature	Tst	-30	+85	°C

Note: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

**7. ELECTRICAL CHARACTERISTICS**

**7.1 Input Power**

ITEM	SYMBOL	MIN	TYP.	MAX	UNIT
Power Supply Voltage	VDD	3.0	3.3	3.6	V
IO Supply Voltage	VDDI	3.0	3.3	3.6	V
Input voltage	Vil	GND	-	0.3VDDI	V
	Vih	0.7VDDI	-	VDDI	V

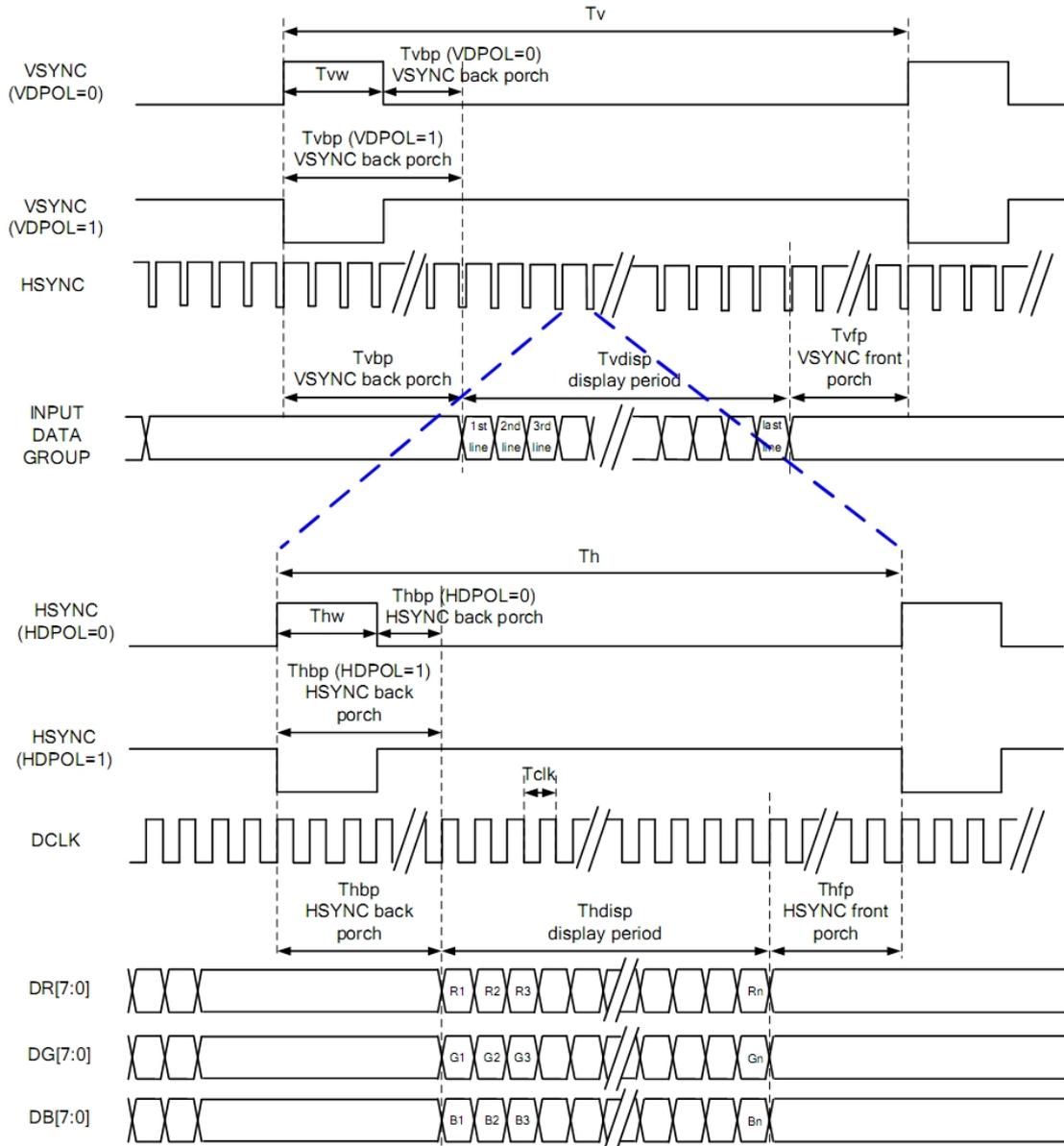
**7.2 Backlight Driving Conditions**

ITEM	SYMBOL	SPECIFICATIONS			UNIT	REMARK
		MIN	TYP.	MAX		
Voltage for LED Backlight	Vf	-	22.40	-	V	I <sub>L</sub> =20mA
Current for LED Backlight	I <sub>L</sub>	-	20	-	mA	-
Power Consumption	P	-	0.448	-	W	-
Led Lifetime	-	-	50.000	-	Hr	Note

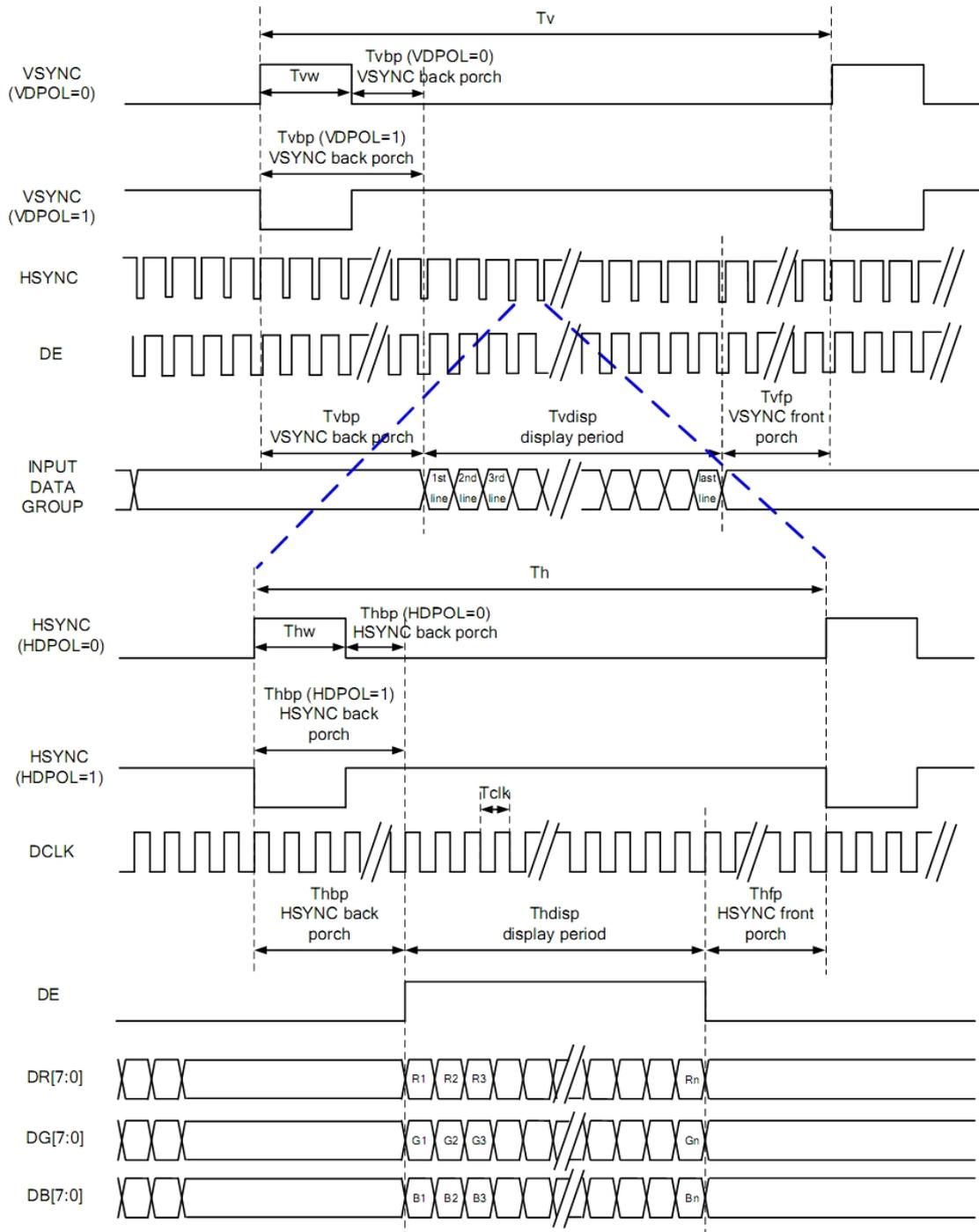
Note: brightness to be decreased to 50% of the initial value at ambient temperature TA=25°C

8. TIMING CHARACTERISTICS

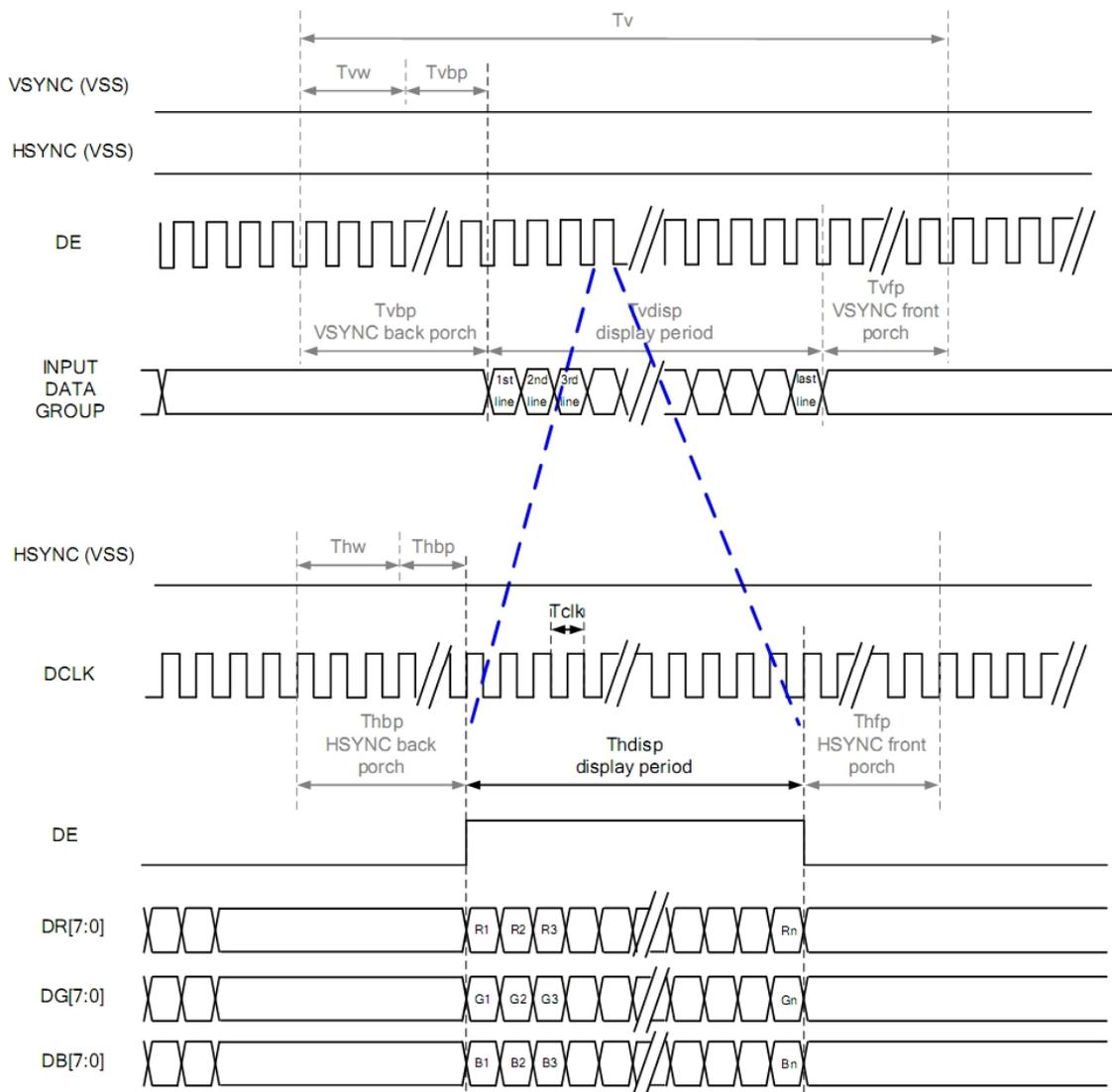
8.1 RGB Interface — SYNC Mode



8.2. RGB Interface — SYNC-DE Mode



8.3. RGB Interface — DE Mode



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

**8.4 Parallel 24 bit RGB Input Timing Table**

Parallel 24-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

480RGB X 272 Resolution Timing Table							
Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
DCLK Frequency	Fclk	8	9	12	MHz		
DCLK Period	Tclk	83	111	125	ns		
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	By H_BLANKING setting
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
VSYNC	Period Time	Tv	276	292	321	HSYNC	
	Display Period	Tvdisp		272		HSYNC	
	Back Porch	Tvbp	2	12	12	HSYNC	By V_BLANKING setting
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

**9. RELIABILITY TEST**

NO.	TEST ITEM	DESCRIPTION
1	High Temperature Operation	Ta=+85°C, 240hrs
2	Low Temperature Operation	Ta=-30°C, 240hrs
3	High Temperature Storage	Ta=+85°C, 240hrs
4	Low Temperature Storage	Ta=-30°C, 240hrs
5	High Temperature and High Humidity (Operating)	Ta=+60°C, 90%RH, 240hrs

**10. LCD MODULES HANDLING PRECAUTIONS**

- n** The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- n** If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If
- n** The substance come into contact with your skin or clothes promptly wash it off using soap and water.
- n** Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- n** The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- n** To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling the LCD module.
  - Tools required for assembly, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- n** Storage precautions  
When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

**11. OTHERS**

- n** Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- n** If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- n** To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
  - Exposed area of the printed circuit board
  - Terminal electrode sections.