

Display Elektronik GmbH

DATA SHEET

TFT MODULE

DEM 480128E VMX-PW-N

3,9" TFT

Product Specification

Version: 0

09.12.2024

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*** Description**

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This module is composed of a Transmissive type TFT-LCD Panel, driver circuit, backlight unit. The resolution of a 3.9 " TFT-LCD contains 480X128 pixels, and can display up to 16.7M colors.

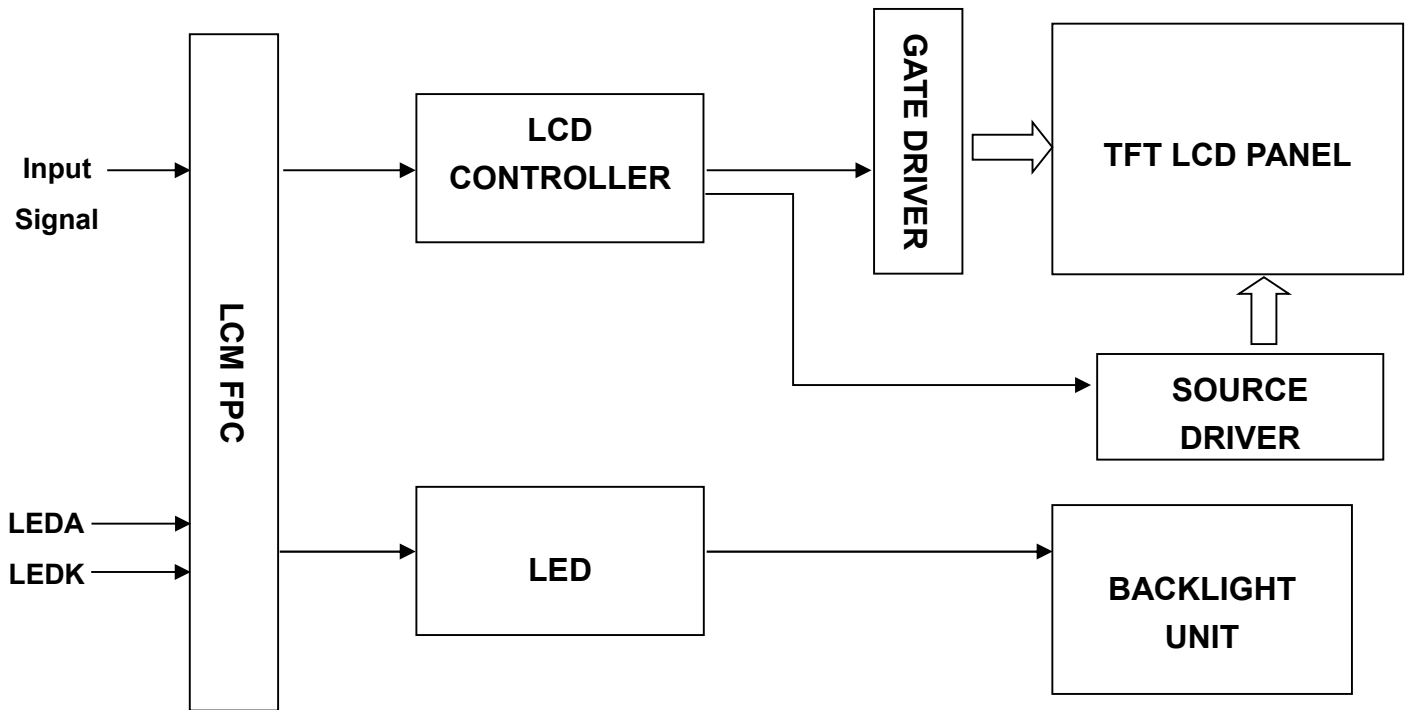
*** Features**

General Information Items	Specification	Unit	Note
	Main Panel		
Display Area	95.04 x 25.34 (3.9 inch)	mm	-
Driver Element	TFT active matrix	-	-
Display Colors	16.7 Million	colors	-
Number of Pixels	480x RGB x 128	dots	-
TFT Pixel Arrangement	RGB Vertical Stripe	-	-
Pixel Pitch	0.198 x 0.198	mm	-
Viewing Angle	ALL	o'clock	-
TFT Controller IC	ST7282A (Sitronix)	-	-
LCM Interface	24-BIT-RGB	-	-
Display Mode	IPS, Transmissive / Normally Black	-	-
Operating Temperature	-30 ~ +85	°C	-
Storage Temperature	-30 ~ +85	°C	-

*** Mechanical Information**

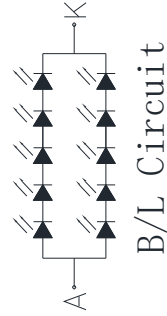
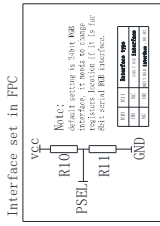
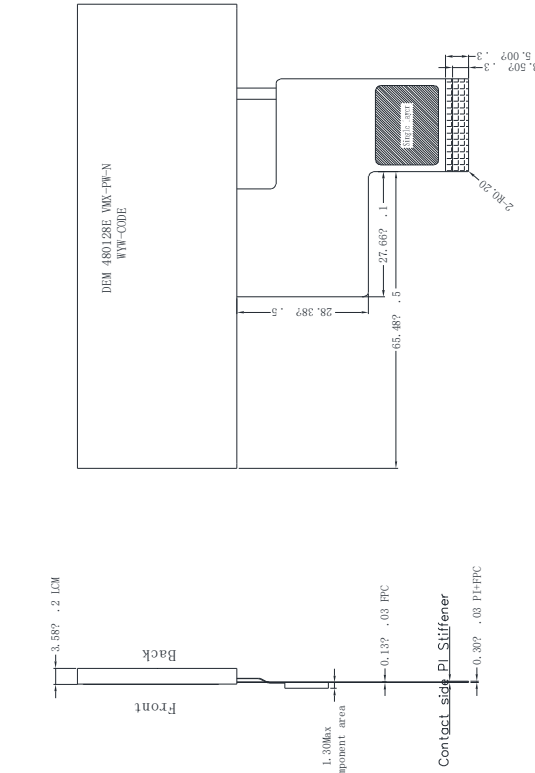
Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	-	102.46	-	mm	-
	Vertical(V)	-	34.4	-	mm	-
	Depth(D)	-	3.58	-	mm	-
Weight		-	19	-	g	-

1. Block Diagram



2. Outline Dimension

Pin	Name
1	VLED+
2	VLED-
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	B5
12	B7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B6
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	PCLK
31	DISP
32	RSYNC
33	VSVC
34	DE
35	NC
36	GND
37	WRNC
38	YDNC
39	VAVO
40	YUNC



- NOTE:
1. DISPLAY TYPE: 3.9", TFT-LCD, 16.7M COLORS
 2. DISPLAY MODE: NORMALLY BLACK, IPS
 3. VIEWING DIRECTION: ALL
 4. LCM DRIVER IC: ST7282A (COG)
LCM Interface: 24BIT RGB
 5. VDD/VCI: 3.3V (TYP.)
 6. OPERATING TEMP: -30°C TO 85°C
STORAGE TEMP: -30°C TO 85°C
 7. BACK LIGHT: LED WHITE, 10 LED, 40mA, 13.5~17.0V
 8. RoHS COMPLIANT.

3. Input terminal Pin Assignment

NO.	SYMBOL	DISCRIPTION	I/O
1	VLED-	Cathode pin OF backlight	P
2	VLED+	Anode pin of backlight	P
3	GND	Ground.	P
4	VDD	Supply voltage(3.3V).	P
5	R0	Red data input.	I
6	R1	Red data input.	I
7	R2	Red data input.	I
8	R3	Red data input.	I
9	R4	Red data input.	I
10	R5	Red data input.	I
11	R6	Red data input.	I
12	R7	Red data input.	I
13	G0	Green data input.	I
14	G1	Green data input.	I
15	G2	Green data input.	I
16	G3	Green data input.	I
17	G4	Green data input.	I
18	G5	Green data input.	I
19	G6	Green data input.	I
20	G7	Green data input.	I
21	B0	Blue data input.	I
22	B1	Blue data input.	I
23	B2	Blue data input.	I
24	B3	Blue data input.	I
25	B4	Blue data input.	I
26	B5	Blue data input.	I

27	B6	Blue data input.	I
28	B7	Blue data input.	I
29	GND	Ground.	P
30	PCLK	Clock signal. Latching data at the rising edge	I
31	DISP	Standby setting for testing, it should be connected to VDD in normal operation mode. If connected to GND, the IC is in standby mode.	I
32	HSYNC	Horizontal Sync input. Negative polarity.	I
33	VSYNC	Vertical Sync input. Negative polarity.	I
34	DE	Data input Enable. Active High to enable the data input Bus under "DE Mode".	I
35	NC	--	--
36	GND	Ground.	P
37	XR(NC)	--	--
38	YD(NC)	--	--
39	XL(NC)	--	--
40	YU(NC)	--	--

4. LCD Optical Characteristics

4.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit.	Note
Contrast Ratio		CR	$\Theta=0$	1000	1200	--		(1)(2)
Response Time	Rising	T_{R+T_F}	Normal viewing angle	--	30	35	msec	(1)(3)
	Falling							
Color Gamut		S(%)		40	45	--	%	
Color Filter Chromaticity	White	W_X		-0.04	0.319	+0.04		(1)(4) CA-310
		W_Y			0.348			
	Red	R_X			0.590			
		R_Y			0.361			
	Green	G_X			0.335			
		G_Y			0.559			
	Blue	B_X			0.160			
		B_Y			0.139			
Viewing Angle	Hor.	Θ_L	CR>10	80	85	--		(1)(4)
		Θ_R		80	85	--		
	Ver.	Θ_U		80	85	--		
		Θ_D		80	85	--		
Option View Direction		ALL						

*The data comes from the LCD specification.

Measuring Condition

Measuring surrounding: dark room

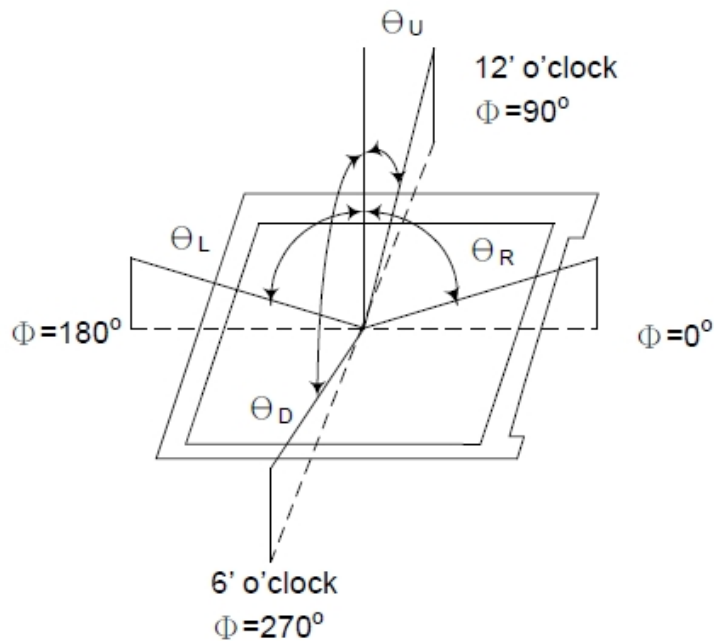
Ambient temperature: 25°C ± 2°C

15min. warm-up time.

Measuring Equipment

FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

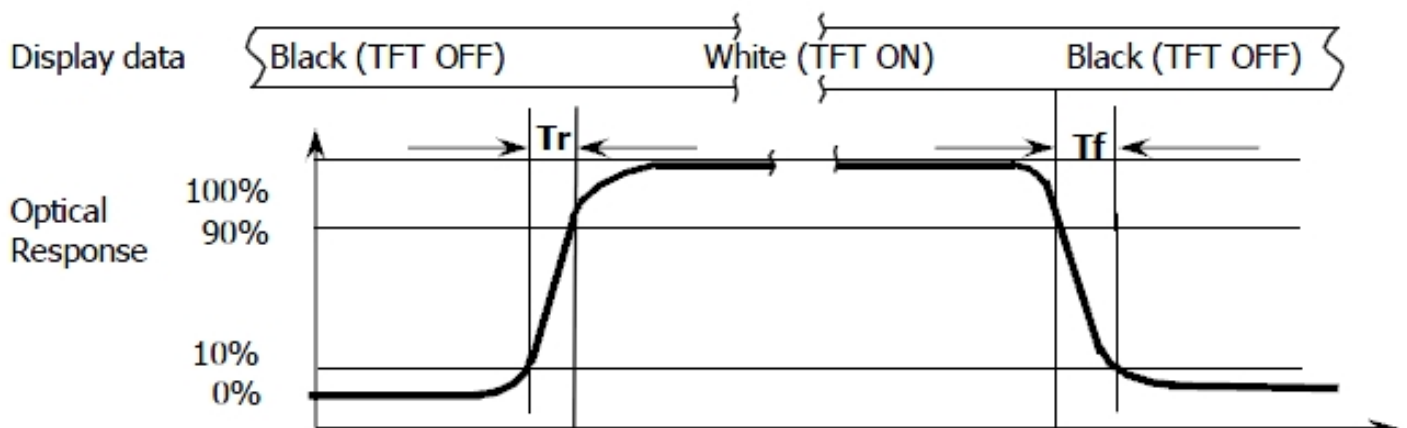
Note (1): Definition of Viewing Angle:



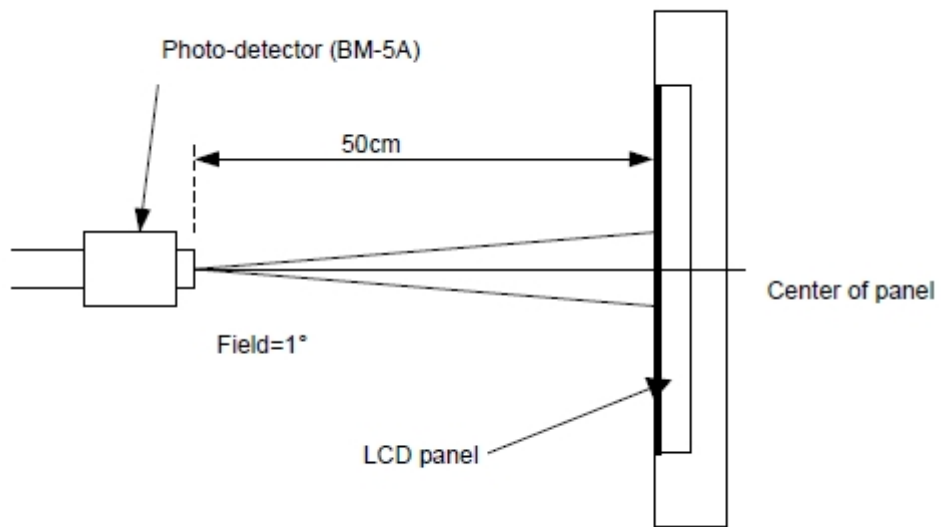
Note (2): Definition of Contrast Ratio(CR): measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3): Response Time



Note (4): Definition of optical measurement setup



5. Electrical Characteristics

5.1 Absolute Maximum Rating (Ta=25 VSS=0V)

Characteristics	Symbol	Min.	Max.	Unit
Digital Supply Voltage	V _{DD}	-0.3	4.0	V
Operating Temperature	T _{OP}	-30	+85	°C
Storage Temperature	T _{ST}	-30	+85	°C

NOTE: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	V _{DD}	3.0	3.3	3.6	V	
Normal Mode Current Consumption	I _{DD}	--	12	24	mA	
Level Input Voltage	V _{IH}	0.7V _{DD}		V _{DD}	V	
	V _{IL}	GND		0.3V _{DD}	V	
Level Output Voltage	V _{OH}	V _{DD} -0.4		V _{DD}	V	
	V _{OL}	GND		GND+0.4	V	

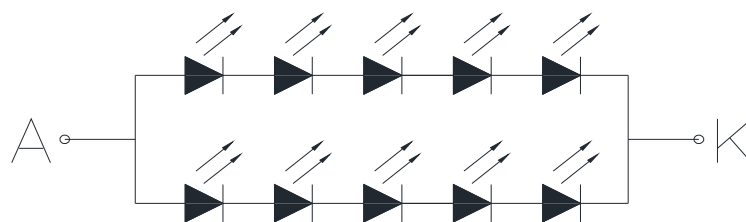
5.3 LED Backlight Characteristics

The backlight system is edge-lighting type with 10 chips White LED

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I _F	--	40	--	mA	
Forward Voltage	V _F	13.5	14.5	17	V	
LCM Luminance (I _F =40mA)	L _v	1700	2000	--	cd/m ²	Note3
LED Lifetime	Hr	50000	--	--	Hour	Note1,2
Uniformity	AVg	80	--	--	%	Note3

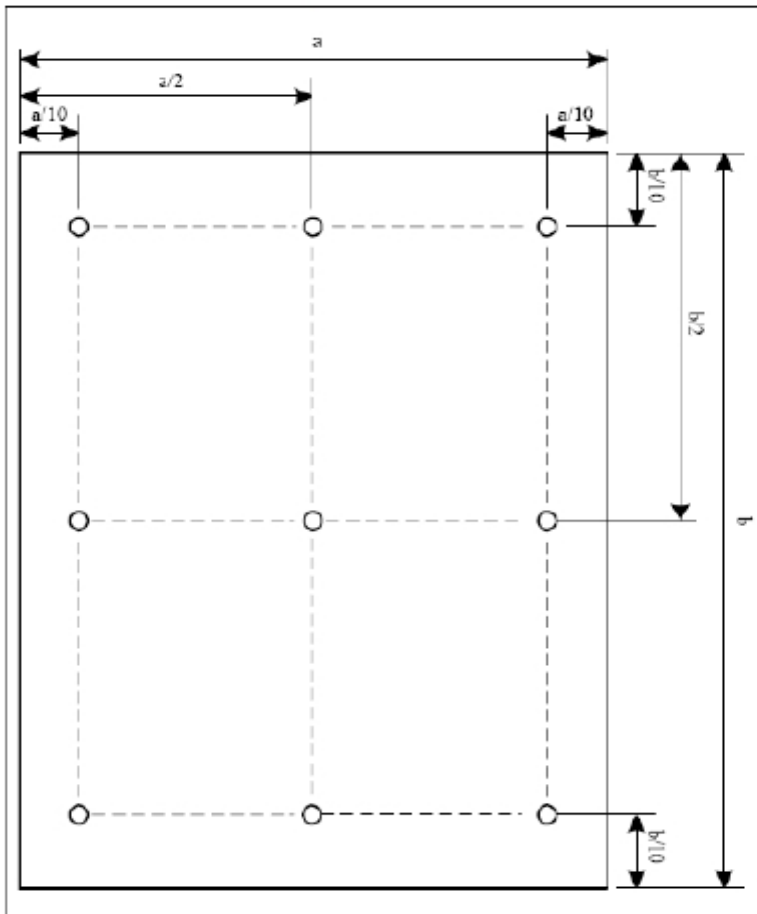
Note (1) LED Lifetime (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25°C ± 3°C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note (2) The “LED Lifetime” is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=40mA. The LED Lifetime could be decreased if operating IL is larger than 40mA. The constant current driving method is suggested.



LED (B/L) CIRCUIT

Note (3): Luminance Uniformity of these 9 points is defined as below:

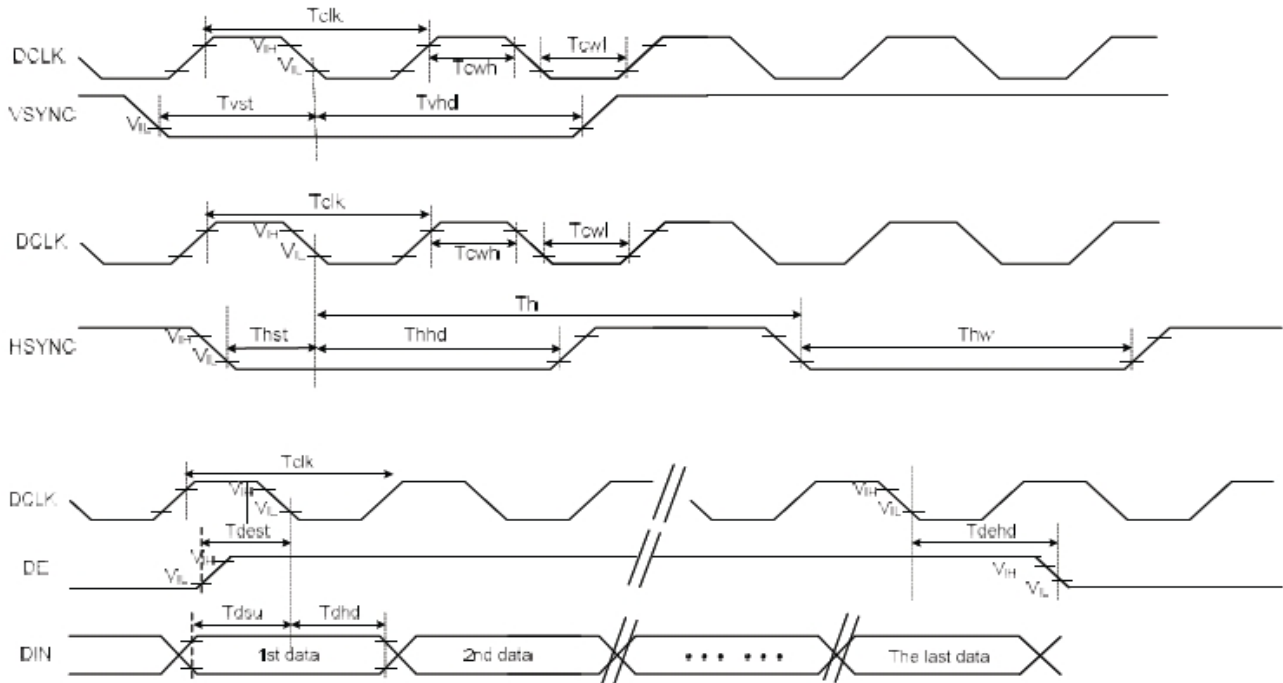


$$\text{Uniformity} = \frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$$

$$\text{Luminance} = \frac{\text{Total Luminance of 9 points}}{9}$$

6. AC Characteristic

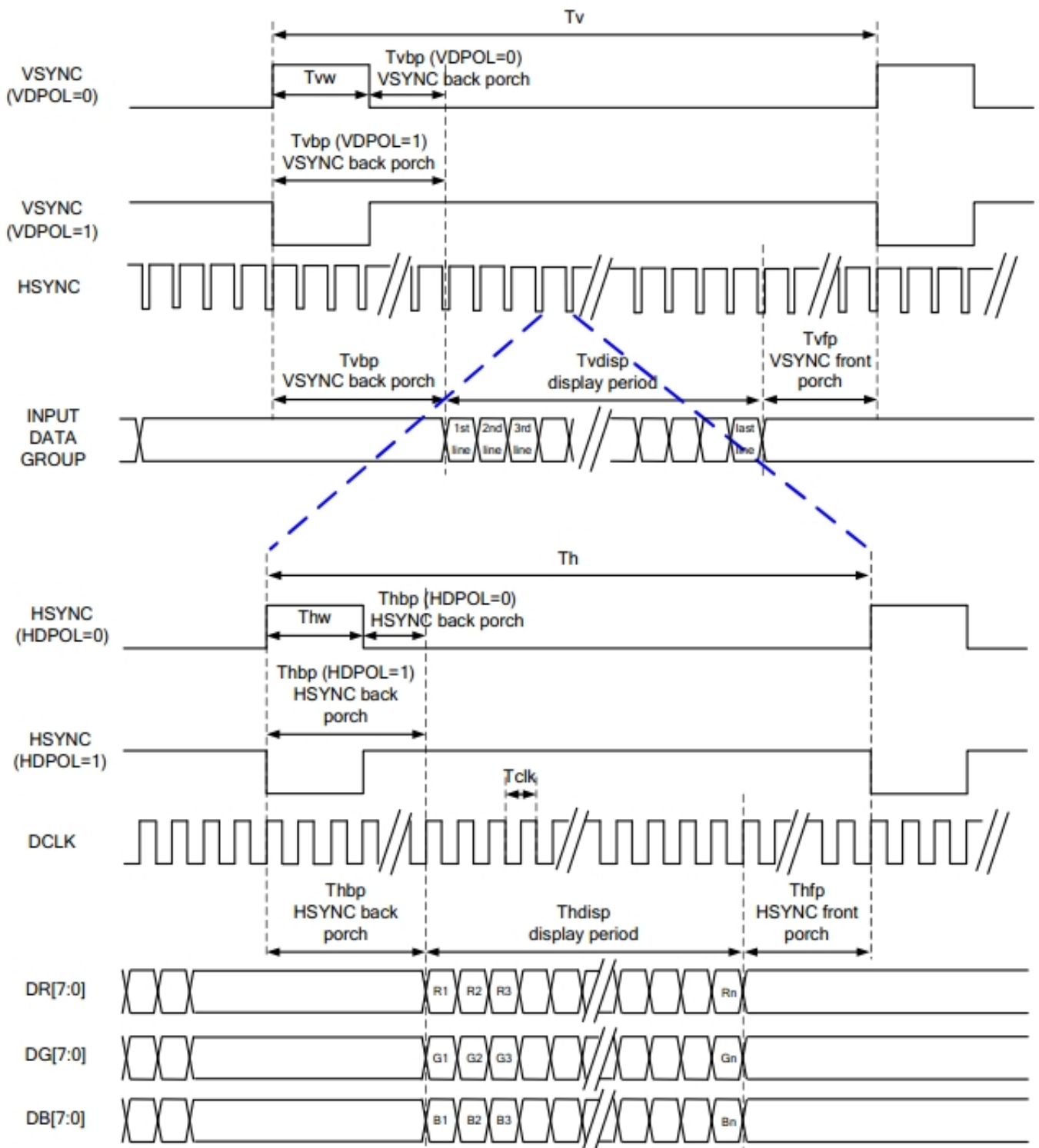
6.1 System Bus Timing for RGB Interface



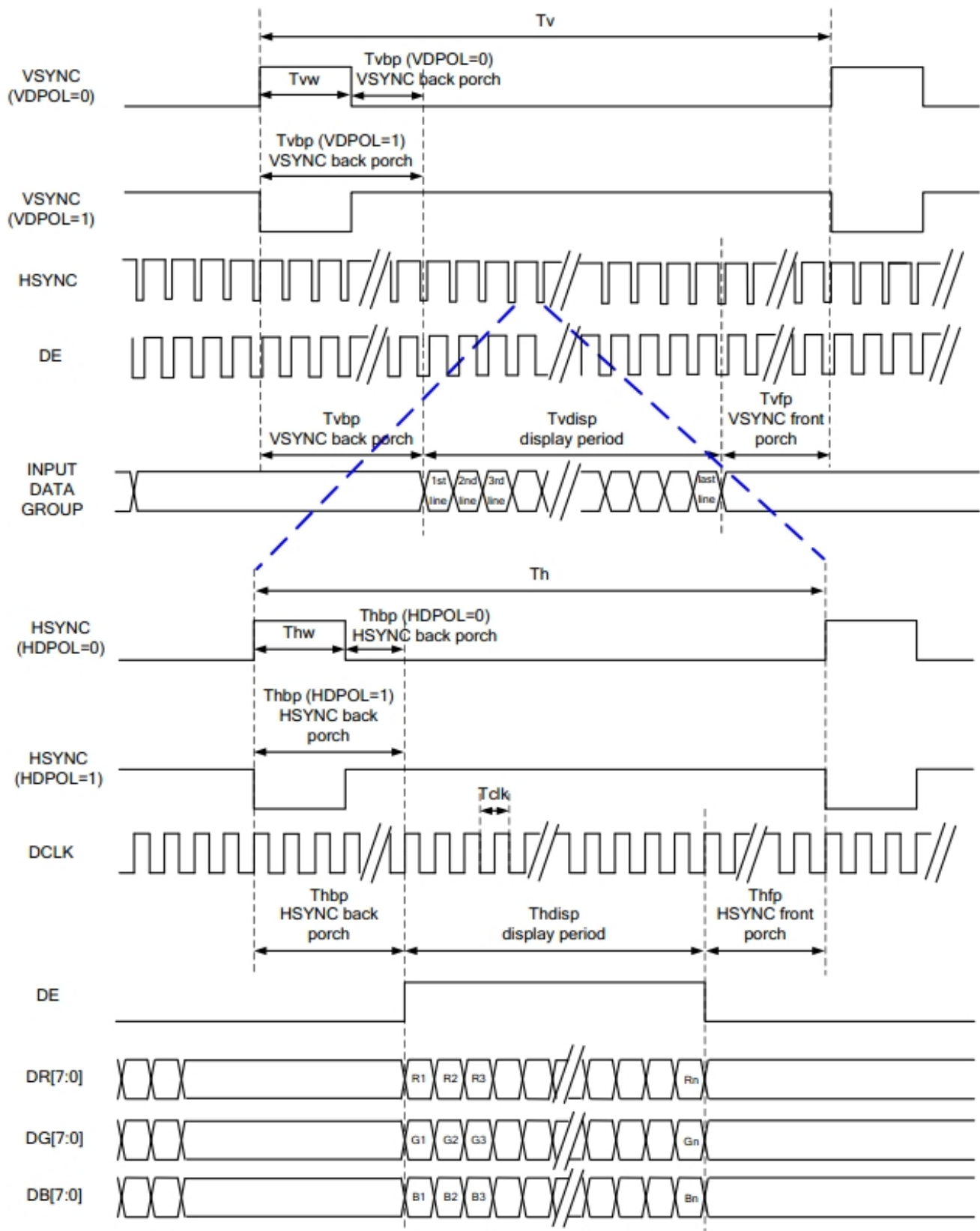
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	T_{cwl}	40	50	60	%	
HSYNC Width	T_{hw}	2	-	-	DCLK	
HSYNC Period	T_h	55	60	65	us	
VSYNC Setup Time	T_{vst}	12	-	-	ns	
VSYNC Hold Time	T_{vhd}	12	-	-	ns	
HSYNC Setup Time	T_{hst}	12	-	-	ns	
HSYNC Hold Time	T_{hhd}	12	-	-	ns	
Data Setup Time	T_{dsu}	12	-	-	ns	
Data Hold Time	T_{dhd}	12	-	-	ns	
DE Setup Time	T_{dest}	12	-	-	ns	
DE Hold Time	T_{dehd}	12	-	-	ns	

6.2 RGB Interface

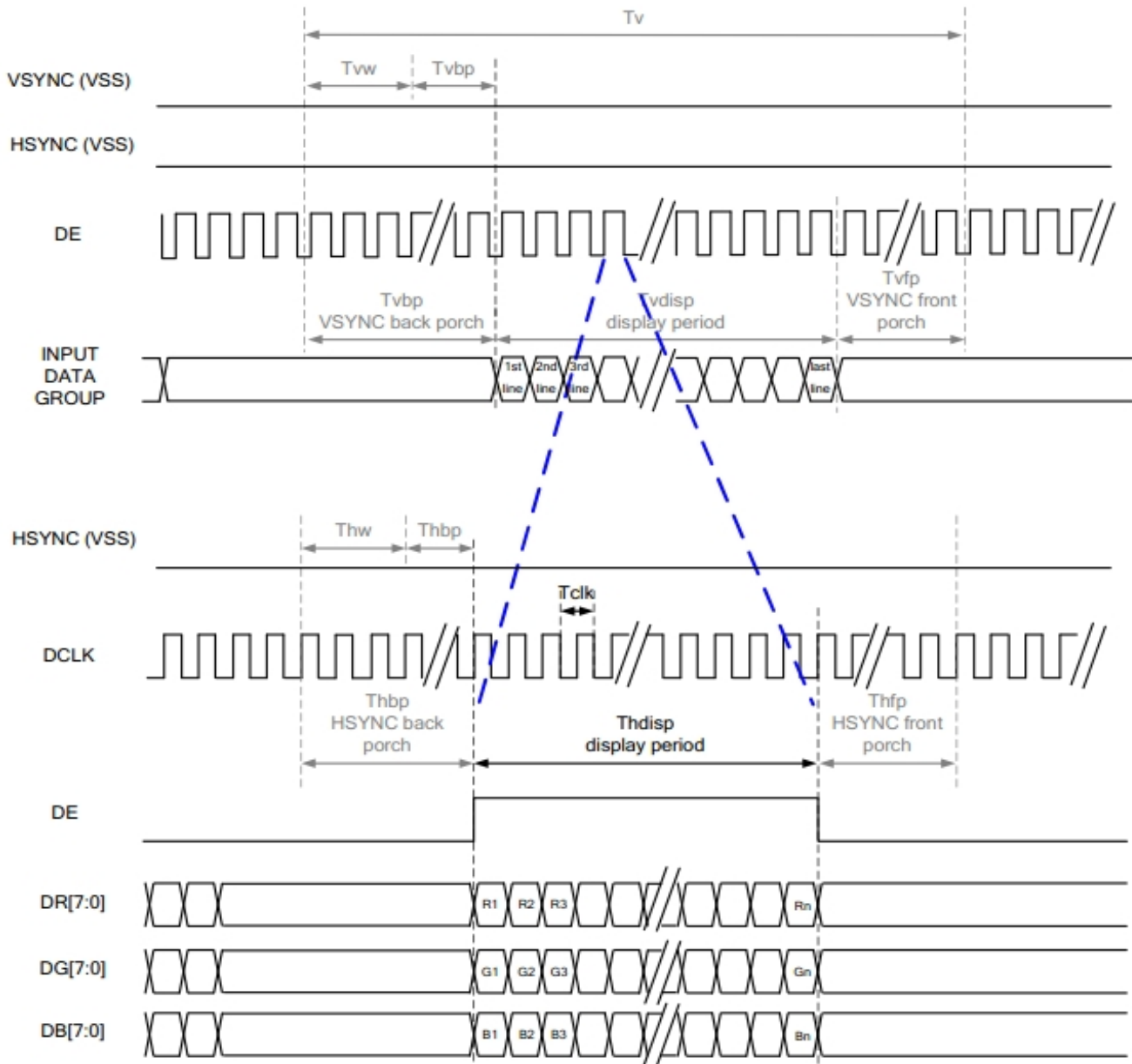
6.2.1 SYNC Mode



6.2.2 SYNC-DE Mode



6.2.3 DE Mode

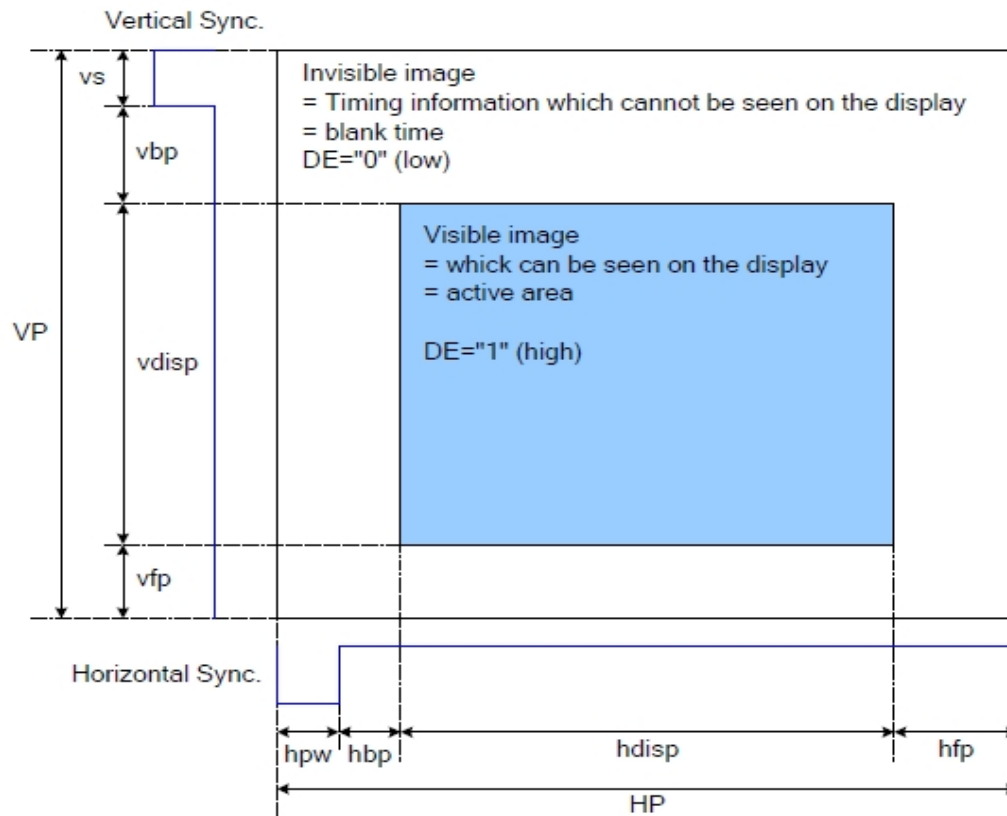


RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

6.3 RGB Input Timing Table

Parallel 24-bit RGB Timing Table

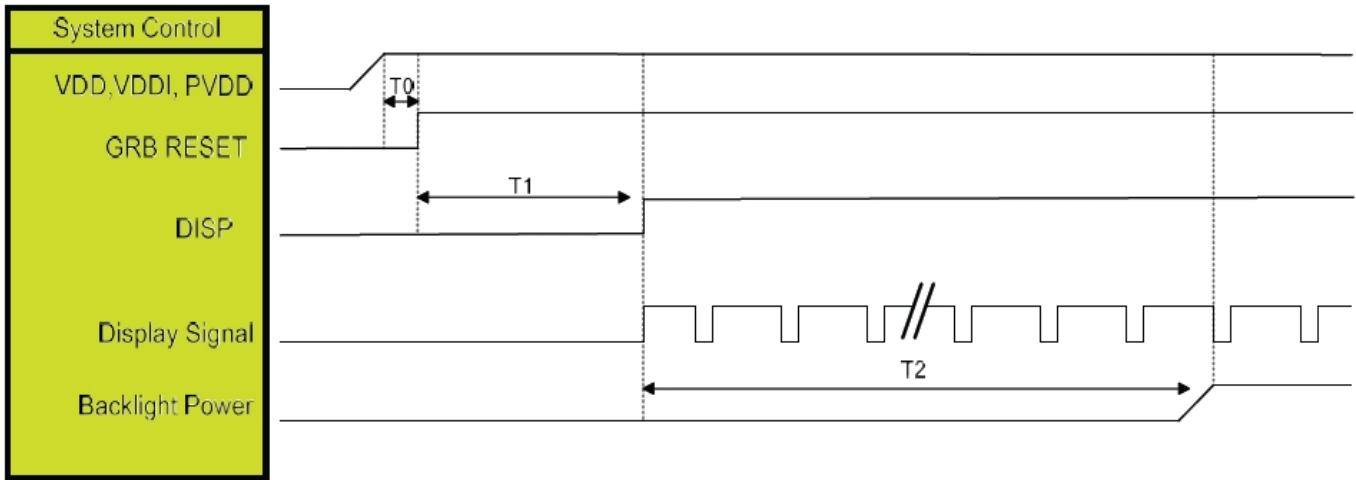


Parameter	Symbol	Min.	Typ.	Max.	Unit
DCLK frequency	FCLK	--	(6)	--	MHz
Horizontal display area	HDISP	--	480	--	Clock
Horizontal Sync. Width	hpw	1	4	--	Clock
Horizontal Sync. Back Porch	hbp	1	60	--	Clock
Horizontal Sync. Front Porch	hfp	1	40	--	Clock
Vertical display area	VDISP	--	128	--	Line
Vertical Sync. Width	vs	1	4	--	Line
Vertical Sync. Back Porch	vbp	1	30	--	Line
Vertical Sync. Front Porch	vfp	1	8	--	Line
Frame-Rate		--	60	--	Hz

Note: 1. Typical value are related to the setting frame rate is 60Hz.

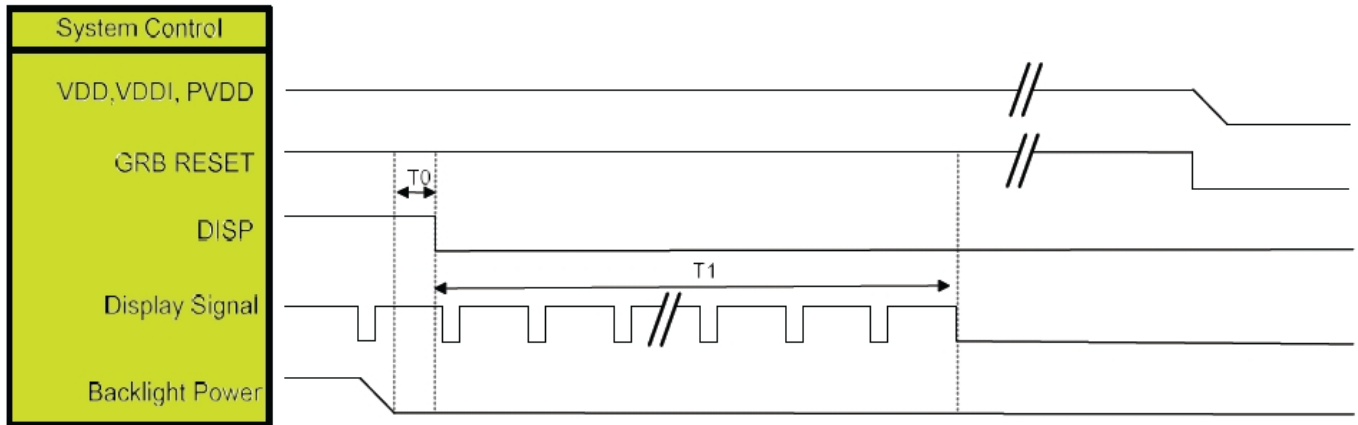
7. LCM POWER ON/OFF SEQUENCE

7.1 Power On Sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

7.2 Power Off Sequence



Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms

8. LCD Module Out-Going Quality Level

8.1 VISUAL & FUNCTION INSPECTION STANDARD

8.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

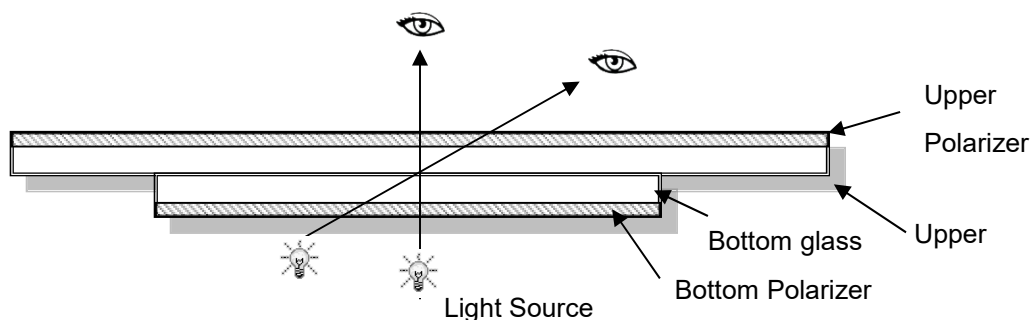
Temperature: 25°C±5°C

Humidity: 65%±10%RH

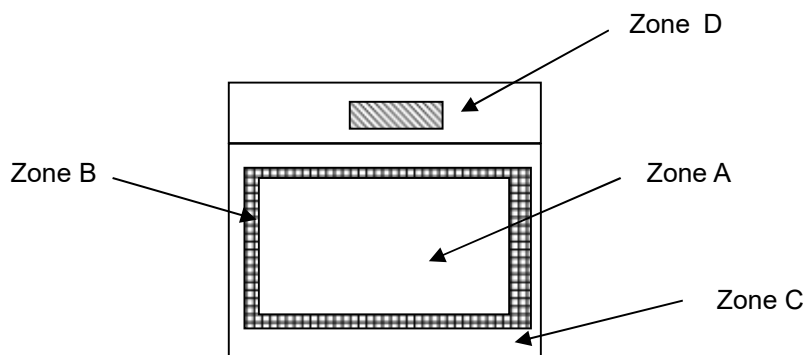
Viewing Angle: Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance: 30-50cm



8.1.2 Definition



Zone A: Effective Viewing Area(Character or Digit can be seen)

Zone B: Viewing Area except Zone A

Zone C: Outside (Zone A+Zone B) which can not be seen after assembly by customer .)

Zone D: IC Bonding Area

Note:

As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer

8.1.3 Sampling Plan

According to GB/T 2828-2012 ; , normal inspection, Class II

AQL:

Major defect	Minor defect
0.65	1.5

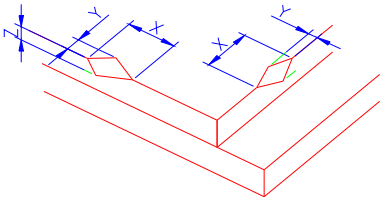
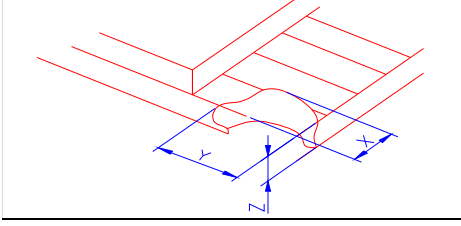
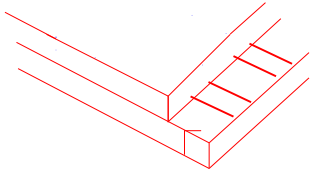
LCD: Liquid Crystal Display, TP: Touch Panel, LCM: Liquid Crystal Module

No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting.	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Spot Line defect	Light dot, Dim spot, Polarizer Bubble ; Polarizer accidented spot.	
6	Soldering appearance	Good soldering , Peeling off is not allowed.	
7	LCD/Polarizer	Black/White spot/line, scratch, crack, etc.	

Note 1:

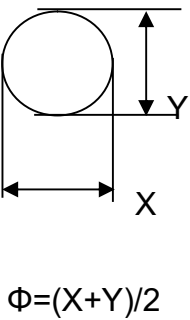
- a) Light dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
- b) Dim dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture.

8.1.4 Criteria (Visual)

Number	Items	Criteria(mm)						
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of ITO, T: Height of LCD	(1) The edge of LCD broken	 <table border="1" data-bbox="715 667 1414 815"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>≤3.0mm</td> <td><Inner border line of the seal</td> <td>≤T</td> </tr> </tbody> </table>	X	Y	Z	≤3.0mm	<Inner border line of the seal	≤T
X	Y	Z						
≤3.0mm	<Inner border line of the seal	≤T						
	(2)LCD corner broken	 <table border="1" data-bbox="794 1122 1334 1223"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>≤3.0mm</td> <td>≤L</td> <td>≤T</td> </tr> </tbody> </table>	X	Y	Z	≤3.0mm	≤L	≤T
X	Y	Z						
≤3.0mm	≤L	≤T						
	(3) LCD crack	 <p style="text-align: center;">Crack Not allowed</p>						

2.0

Spot defect



① light dot (black/white spot , pinhole, stain, etc.)

Zone Size (mm)	Acceptable Qty		
	A	B	C
$\Phi \leq 0.15$	Ignore		
$0.15 < \Phi \leq 0.25$	3(distance ≥ 10 mm)		
$0.25 < \Phi \leq 0.4$	2(distance ≥ 10 mm)		
$\Phi > 0.4$	0		

② Dim spot (light leakage, dent, dark spot, etc)

Zone Size (mm)	Acceptable Qty		
	A	B	C
$\Phi \leq 0.15$	Ignore		
$0.15 < \Phi \leq 0.25$	3(distance ≥ 10 mm)		
$0.25 < \Phi \leq 0.4$	2(distance ≥ 10 mm)		
$\Phi > 0.4$	0		


③ Polarizer accidented spot

Zone Size (mm)	Acceptable Qty		
	A	B	C
$\Phi \leq 0.2$	Ignore		
$0.2 < \Phi \leq 0.5$	2(distance ≥ 10 mm)		
$\Phi > 0.5$	0		

④ Polarizer Bubble

Zone Size (mm)	Acceptable Qty		
	A	B	C
$\Phi \leq 0.2$	Ignore		
$0.2 < \Phi \leq 0.4$	3(distance ≥ 10 mm)		
$\Phi > 0.4$	0		

3.0	LCD Pixel defect	<p>Pixel bad points</p> <table border="1"> <thead> <tr> <th data-bbox="496 309 691 360">Item</th> <th data-bbox="697 309 1203 360">Zone A</th> <th data-bbox="1209 309 1444 360">Acceptable Qty</th> </tr> </thead> <tbody> <tr> <td data-bbox="496 369 691 517" rowspan="3">Bright dot</td> <td data-bbox="697 369 1203 421">Random</td> <td data-bbox="1209 369 1444 421">N≤2</td> </tr> <tr> <td data-bbox="697 421 1203 472">2 dots adjacent</td> <td data-bbox="1209 421 1444 472">N≤0</td> </tr> <tr> <td data-bbox="697 472 1203 524">3 dots adjacent</td> <td data-bbox="1209 472 1444 524">N≤0</td> </tr> <tr> <td data-bbox="496 533 691 680" rowspan="3">Dark dot</td> <td data-bbox="697 533 1203 584">Random</td> <td data-bbox="1209 533 1444 584">N≤2</td> </tr> <tr> <td data-bbox="697 584 1203 636">2 dots adjacent</td> <td data-bbox="1209 584 1444 636">N≤0</td> </tr> <tr> <td data-bbox="697 636 1203 687">3 dots adjacent</td> <td data-bbox="1209 636 1444 687">N≤0</td> </tr> <tr> <td data-bbox="496 696 691 994">Distance</td> <td data-bbox="697 696 1203 994"> 1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot. </td> <td data-bbox="1209 696 1444 994">5mm</td> </tr> <tr> <td colspan="2" data-bbox="496 1003 1203 1055">Total bright and dark dot</td> <td data-bbox="1209 1003 1444 1055">N≤4</td> </tr> </tbody> </table> <p data-bbox="496 1064 576 1099">Note:</p> <p data-bbox="496 1117 1437 1207">A) Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.</p> <p data-bbox="496 1225 1394 1314">B) Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture.</p> <p data-bbox="496 1368 987 1404">C) 2 dot adjacent = 1 pair = 2 dots</p> <p data-bbox="496 1422 608 1458">Picture:</p> <div data-bbox="624 1507 700 1568"> </div> <p data-bbox="541 1610 750 1646">2 dot adjacent</p> <div data-bbox="1031 1507 1142 1568"> </div> <p data-bbox="983 1610 1192 1646">2 dot adjacent</p> <div data-bbox="632 1675 673 1785"> </div> <p data-bbox="496 1803 831 1839">2 dot adjacent (vertical)</p> <div data-bbox="1038 1675 1222 1785"> </div> <p data-bbox="975 1803 1278 1839">2 dot adjacent (slant)</p>	Item	Zone A	Acceptable Qty	Bright dot	Random	N≤2	2 dots adjacent	N≤0	3 dots adjacent	N≤0	Dark dot	Random	N≤2	2 dots adjacent	N≤0	3 dots adjacent	N≤0	Distance	1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot.	5mm	Total bright and dark dot		N≤4
Item	Zone A	Acceptable Qty																							
Bright dot	Random	N≤2																							
	2 dots adjacent	N≤0																							
	3 dots adjacent	N≤0																							
Dark dot	Random	N≤2																							
	2 dots adjacent	N≤0																							
	3 dots adjacent	N≤0																							
Distance	1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot.	5mm																							
Total bright and dark dot		N≤4																							

4.0	Line defect (LCD /Polarizer backlight black/white line, scratch, stain)  W: width, L : length N : Count	<table border="1"> <thead> <tr> <th rowspan="2">Width(mm)</th> <th rowspan="2">Length(m)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.05$</td> <td>Ignore</td> <td colspan="2">Ignore</td> <td rowspan="3">Ignore</td> </tr> <tr> <td>$0.05 < W \leq 0.06$</td> <td>$L \leq 4.0$</td> <td colspan="2">$N \leq 3$</td> </tr> <tr> <td>$0.06 < W \leq 0.08$</td> <td>$L \leq 3.0$</td> <td colspan="2">$N \leq 2$</td> </tr> <tr> <td>$W > 0.08$</td> <td colspan="4">Define as spot defect</td> </tr> </tbody> </table>	Width(mm)	Length(m)	Acceptable Qty			A	B	C	$\Phi \leq 0.05$	Ignore	Ignore		Ignore	$0.05 < W \leq 0.06$	$L \leq 4.0$	$N \leq 3$		$0.06 < W \leq 0.08$	$L \leq 3.0$	$N \leq 2$		$W > 0.08$	Define as spot defect			
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$W > 0.08$	Define as spot defect																											
5.0	Electronic Components SMT.	Not allow missing parts, solderless connection, cold solder joint, mismatch, The positive and negative polarity opposite																										
6.0	Display color& Brightness.	1. Color: Measuring the color coordinates, The measurement standard according to the datasheet or samples. 2. Brightness: Measuring the brightness of White screen, The measurement standard according to the datasheet or Samples.																										
7.0	LCD Mura/Waving/ Hot spot	Not visible through 5% ND filter in 50% gray or judge by limit sample if necessary.																										

Criteria (functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed

9. Reliability Test Result

Item	Condition	Inspection after test
High Temperature	+85°C,96H	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Non-display; 3.Missing segments/line; 4.Glass crack; 5.Current IDD is twice higher than initial value.
Low Temperature	-30°C, 96HR	
High Temperature	+85°C, 96HR	
Low Temperature	-30°C, 96HR	
High Temperature & High Humidity Operating	+60°C, 90% RH ,96 hours.	
Thermal Shock (Non- operation)	-10°C,30 min ↔ +60°C,30 min, Change time: 5min 20CYC.	
ESD Test	C=150pF, R=330,5points/panel Air:±8kV, 5times; Contact:±6kV, 5 times; (Environment: 15°C~35°C, 30%~60%).	
Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total) (Package	
Box Drop Test	1 Corner 3 Edges 6 faces,80cm(MEDIUM BOX)	

Remark:

1. The test samples should be applied to only one test item.
2. Sample size for each test item is 5~10pcs.
3. For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
4. In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
5. Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.
6. The color fading mura of polarizing filter should not care.

10. Cautions and Handling Precautions

10.1 Handling and Operating the Module

(1) When the module is assembled, it should be attached to the system firmly.

Do not warp or twist the module during assembly work.

(2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

(3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.

(4) Do not allow drops of water or chemicals to remain on the display surface.

If you have the droplets for a long time, staining and discoloration may occur.

(5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

(6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.

Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.

(8) Protect the module from static; it may cause damage to the CMOS ICs.

(9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(10) Do not disassemble the module.

(11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(12) Pins of I/F connector shall not be touched directly with bare hands.

(13) Do not connect, disconnect the module in the "Power ON" condition.

(14) Power supply should always be turned on/off by the item 6.1 Power On Sequence & 6.2 Power Off Sequence

10.2 Storage and Transportation.

(1) Do not leave the panel in high temperature, and high humidity for a long time.

It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%

(2) Do not store the TFT-LCD module in direct sunlight.

(3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.

(4) It is recommended that the modules should be stored under a condition where no condensation is allowed.

Formation of dewdrops may cause an abnormal operation or a failure of the module.

In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.

(5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.