

DISPLAY Elektronik GmbH

DATA SHEET

LCD MODULE

DEM 320240A2 TMH-PW-N

Product Specification

Version:1

09.07.2026

GENERAL SPECIFICATION

MODULE NO. :

DEM 320240A2 TMH-PW-N
CUSTOMER

VERSION NO.	CHANGE DESCRIPTION	DATE
0	ORIGINAL VERSION	26.06.2026
1	UPDATE THE DRAWING	09.07.2026

PREPARED BY: LM

DATE: 09.07.2026

APPROVED BY: WH

DATE: 09.07.2026

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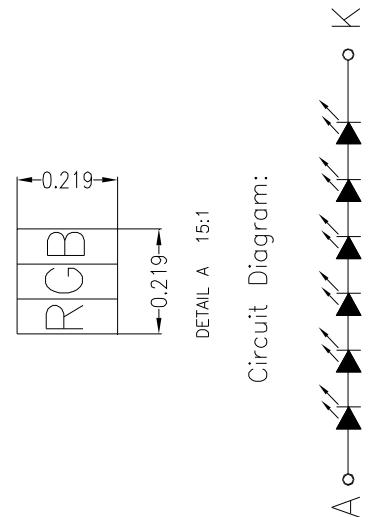
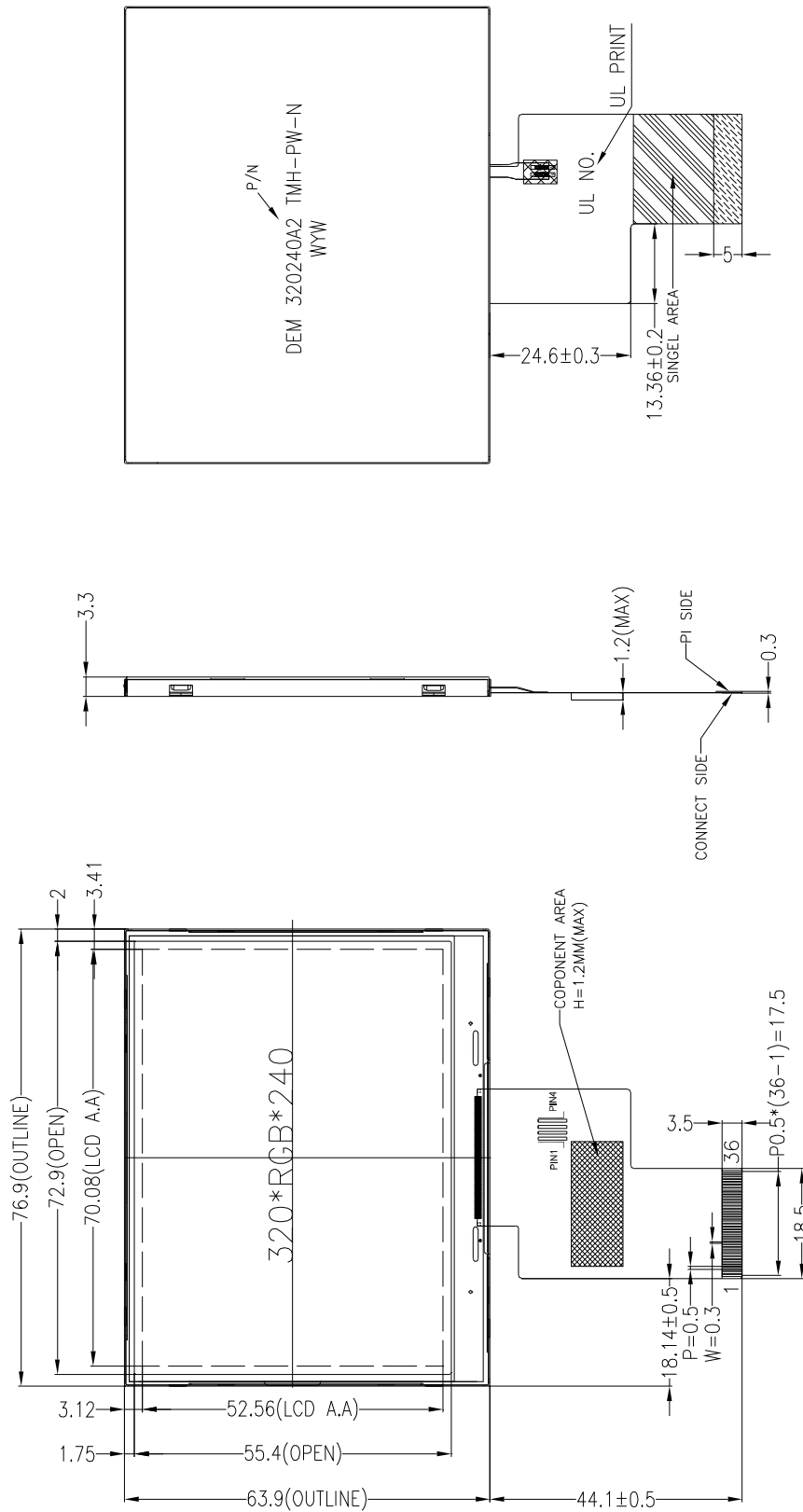
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1. GENERAL SPECIFICATIONS

Item	Contents	Unit
LCD TYPE	TFT/TRANSMISSIVE	-
MODULE SIZE(W*H*T)	76.90 x 63.90 x 3.30	mm
ACTIVE SIZE(W*H)	70.08 x 52.56	mm
PIXEL PITCH(W*H)	0.219 x 0.219	mm
NUMBER OF DOTS	320 x RGB x 240	-
DIVER IC	SSD2119	-
INTERFACE TYPE	8-BIT MCU	-
TOP POLARIZER TYPE	ANTI-GLARE	-
RECOMMEND VIEWING DIRECTION	12:00	O'CLOCK
GRAY SCALE INVERSION DIRECTION	6:00	O'CLOCK
COLORS	262k	-
BACKLIGHT TYPE	6-DIES WHITE LED	-
TOUCH PANEL TYPE	WITHOUT	-

2. EXTERNAL DIMENSIONS



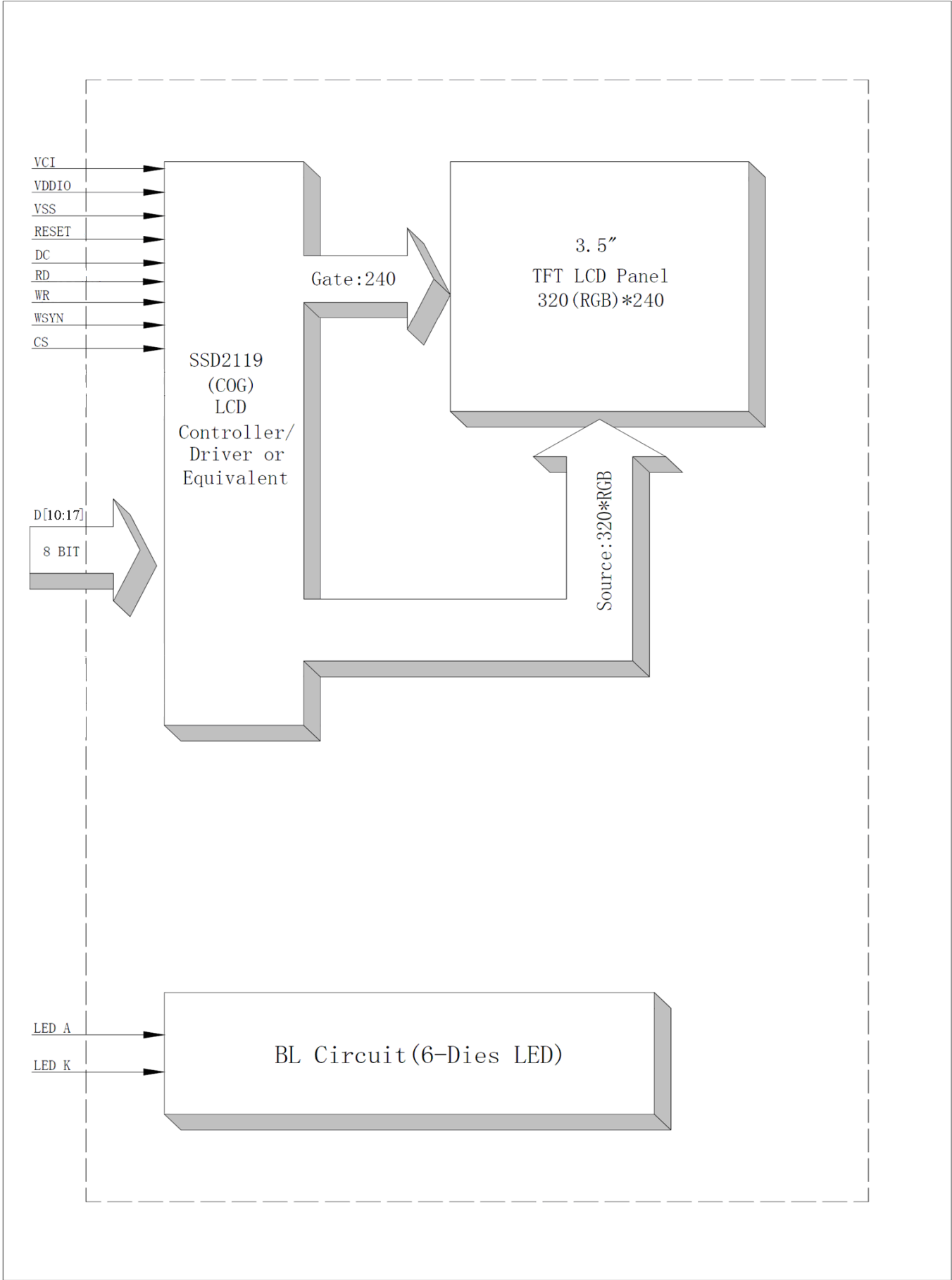
DETAIL A 15:1

Circuit Diagram:

Remark:

1. Unmarked tolerance is ± 0.3
 2. All materials comply with RoHS
 3. ...critical dimension
 4. WW: Product date code printing
- Format: Y:year, WW:week

3. BLOCK DIAGRAM

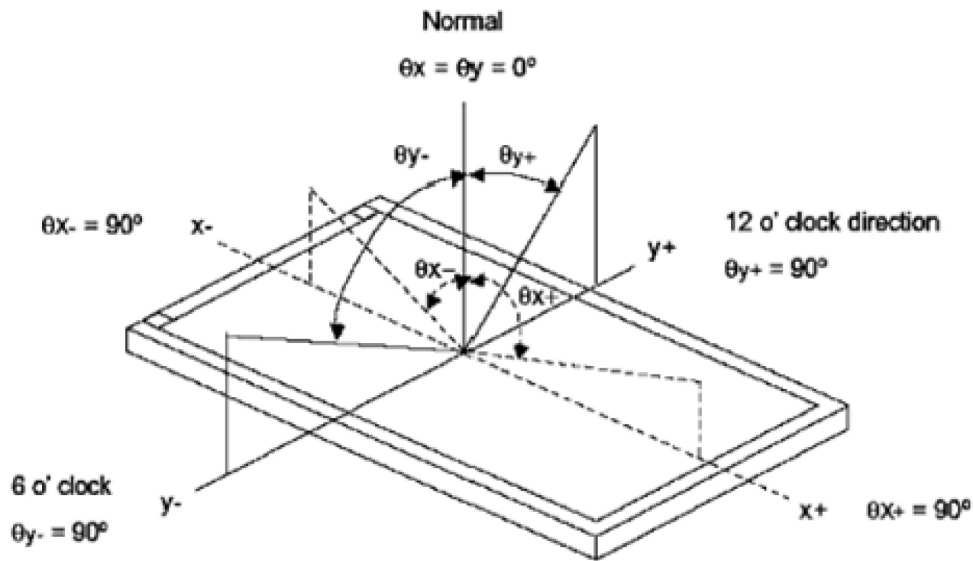


4. PIN ASSIGNMENT

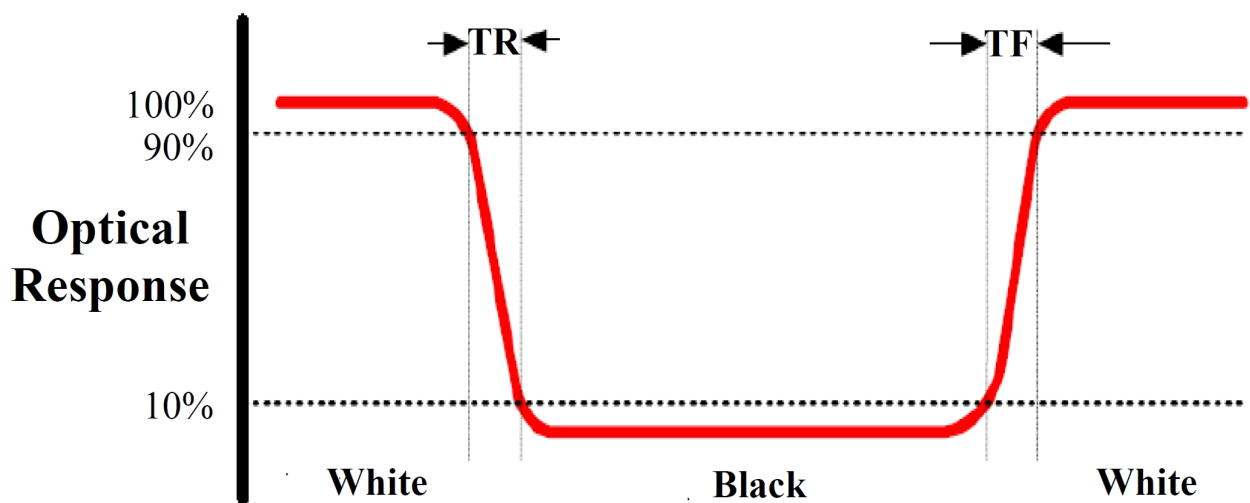
NO.	SYMBOL	DESCRIPTION	I/O
1	LED_A	Anode of LED Backlight	P
2	LED_K	Cathode of LED Backlight	P
3	VDDIO	Voltage input pin for logic I/O	P
4	NC	No Connect	I
5	NC	No Connect	I
6	NC	No Connect	I
7	NC	No Connect	I
8	NC	No Connect	I
9	NC	No Connect	I
10	NC	No Connect	I
11	NC	No Connect	I
12	NC	No Connect	I
13	NC	No Connect	I
14~21	DB10~DB17	Data Bus	I
22	WSYN	Ram Write Synchronization output-Leave it OPEN when not used	I
23	GND	Ground	P
24	/CS	Chip select pin	I
25	/WR	Write strobe signal	I
26	/RD	Read strobe signal	I
27	DC	A register select signal	I
28	/RESET	System reset pin	I
29	VDDIO	Voltage input pin for logic I/O	P
30	VCI	Power supply for analog	P
31	NC(XL)	No connect(Touch panel XL)	I
32	NC(YD)	No connect(Touch panel YD)	I
33	NC(XR)	No connect(Touch panel XR)	I
34	NC(YU)	No connect(Touch panel YU)	I
35	GND	Ground	P
36	GND	Ground	P

5. OPTICAL CHARACTERISTICS

Item	Symbol		Condition s	Specifications			Unit
				Min	Typ.	Max	
Luminance	L		Il=20mA	-	450	-	cd/m²
Contrast Ratio	CR		θ =0 °	300	-	-	
Response Time	Ton		25°C	-	10	-	ms
	Toff			-	15	-	
CIE COLOUR COORDINATE	RED	XR	Viewing normal angle	0.633	0.653	0.673	-
		YR		0.310	0.330	0.350	-
	GREEN	XG		0.292	0.312	0.332	-
		YG		0.552	0.572	0.592	-
	BLUE	XB		0.118	0.138	0.158	-
		YB		0.112	0.132	0.152	-
	WHITE	XW		0.286	0.306	0.326	-
		YW		0.321	0.341	0.361	-
VIEWING ANGLE	Hor.	θ x+	CR≥10	-	60	-	Degree
		θ x-		-	60	-	
	Ver.	θ y+		-	50	-	
		θ y+		-	60	-	
Uniformity	Un		-	-	80	85	%

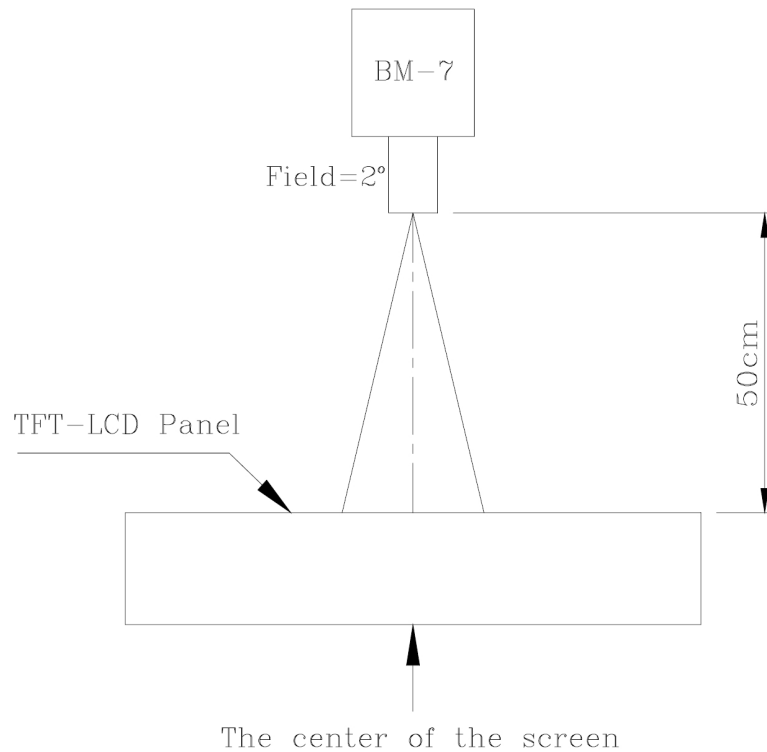
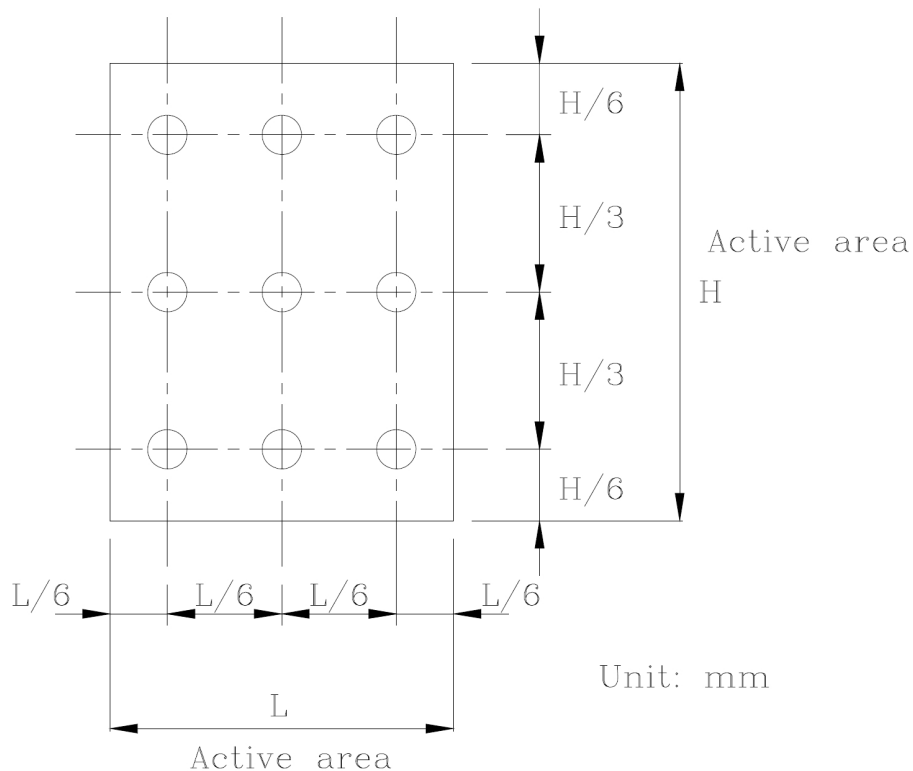
Note 1: Definition of Viewing Angle θ_x and θ_y :**Note 2: Definition of contrast ratio CR:**

$$CR = \frac{\text{Luminance of white state}}{\text{Luminance of black state}}$$

Note 3: Definition of Response Time (T_r, T_f)

Note 4: Definition of Luminance**①The Brightness Test Equipment Setup**

Field=2° (As measuring “black” image, field=2° is the best testing condition)

**②The Brightness Test Point Setup**

6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	Max	Unit
Supply Voltage for Analog	VCI	-0.3	4.5	V
Supply Voltage for Logic	VDDIO	-0.3	4.5	V
Supply Current (one LED)	I(LED)		30	mA
Operating Temperature	Top	-20	+70	°C
Storage Temperature	Tst	-30	+80	°C

Note: The absolute maximum rating values of this product are not allowed to be exceeded at any times.

Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

7. ELECTRICAL CHARACTERISTICS

7.1 Electrical Characteristics

ITEM	Symbol	Min	Typ.	Max	Unit
Supply Voltage for Analog	VDD	3.0	3.3	3.6	V
Supply Voltage for Logic	VDD	3.0	3.3	3.6	V
Input Voltage	Vil	GND	-	0.3VCI	V
	Vih	0.7VCI	-	VCI	V
Input Leakage Current	Ilkg	-1	-	1	uA

7.2 Backlight Driving Conditions

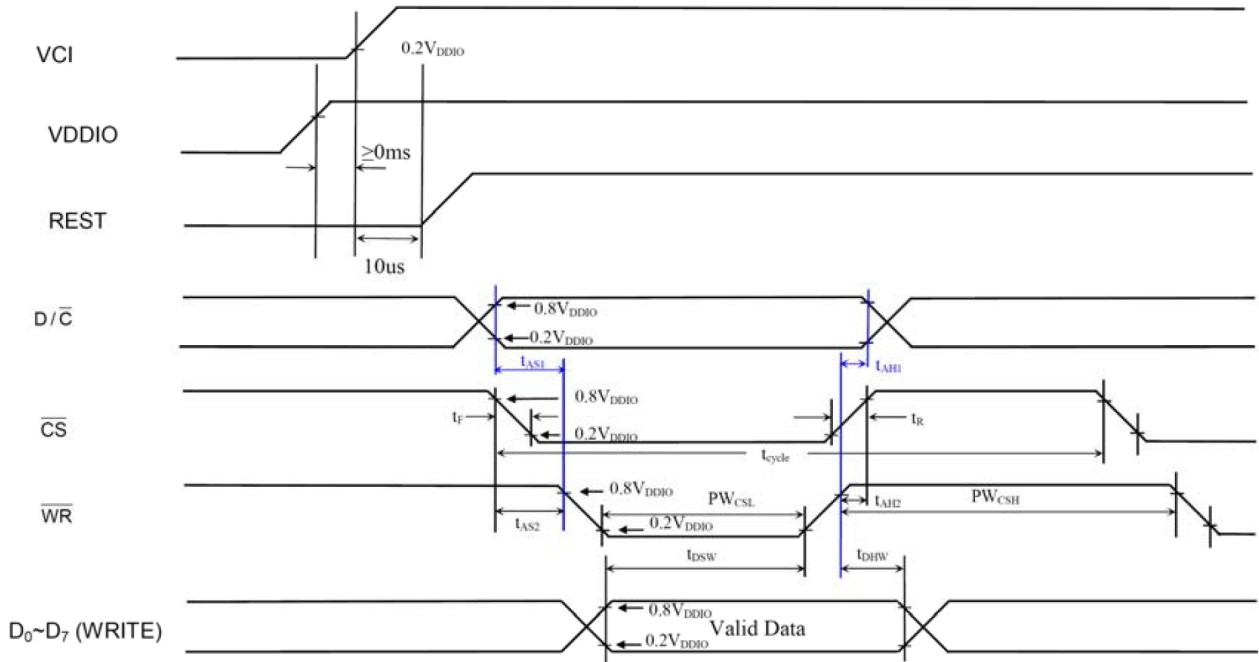
Item	Symbol	Specifications			Unit	Remark
		Min	Typ.	Max		
Voltage for LED Backlight	Vf	-	19.2	-	V	Il=20mA
Current for LED Backlight	Il	-	20	30	mA	-
Power Consumption	P	-	0.384	-	W	-
Led Lifetime	-	30.000	50.000	-	Hr	Note

Note: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25°C

8. TIMING CHARACTERISTICS

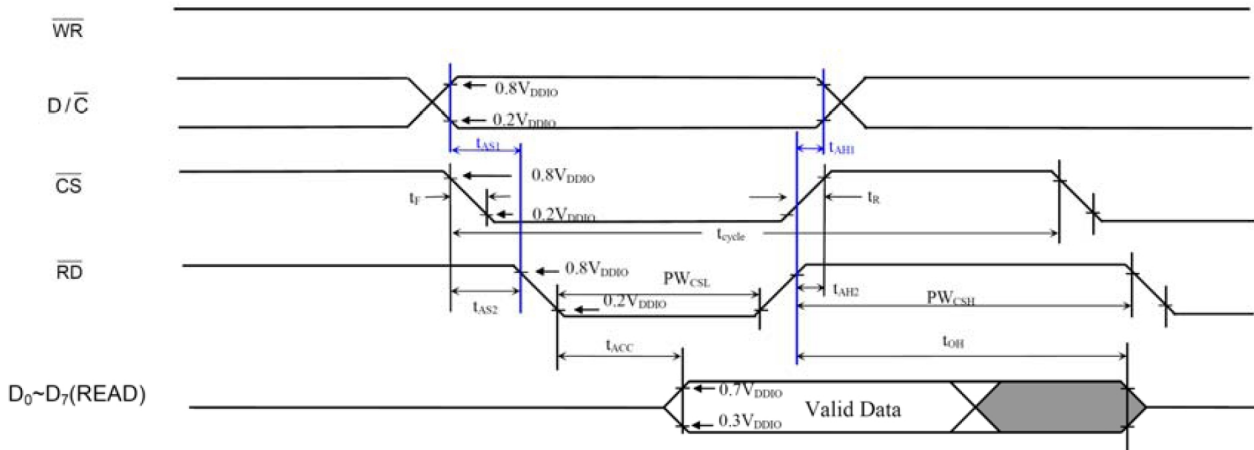
MCU Mode Timing Diagram

Write Cycle



Remark: It's highly recommended that $\overline{RD}$ remains high for the whole write cycle

Read Cycle



Symbol	Parameter	Min	Typ	Max	Unit
t_{cycle}	Clock Cycle Time (write cycle)	75	-	-	ns
t_{cycle}	Clock Cycle Time (read cycle) (Based on $V_{OL}/V_{OH} = 0.3 \cdot V_{DDIO}/0.7 \cdot V_{DDIO}$)	450	-	-	ns
t_{AS1}	Address Setup Time between $(R/\overline{W})$ and $D/\overline{C}$	0	-	-	ns
t_{AH1}	Address Hold Time between $(R/\overline{W})$ and $D/\overline{C}$	0	-	-	ns
t_{AS2}	Address Setup Time between $(R/\overline{W})$ and $\overline{CS}$	0	-	-	ns
t_{AH2}	Address Hold Time between $(R/\overline{W})$ and $\overline{CS}$	0	-	-	ns
t_{DSW}	Data Setup Time ($D0-D7$, WRITE)	5	-	-	ns
t_{DHW}	Data Hold Time ($D0-D7$, WRITE)	5	-	-	ns
t_{ACC}	Data Access Time ($D0-D7$, READ)	250	-	-	ns
t_{OH}	Output Hold time ($D0-D7$, READ)	100	-	-	ns
PW_{CSL}	Pulse width /CS low (write cycle)	40	-	-	ns
PW_{CSH}	Pulse width /CS high (write cycle)	25	-	-	ns
PW_{CSL}	Pulse width /CS low (read cycle)	500	-	-	ns
PW_{CSH}	Pulse width /CS high (read cycle)	500	-	-	ns
t_R	Rise time	-	-	4	ns
t_F	Fall time	-	-	4	ns

9. RELIABILITY TEST

9.1 Standard Specification for Reliability of LCD Module

No.	Test Item	Description
1	High temperature operation	The sample should be allowed to stand at 70°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
2	Low temperature operation	The sample should be allowed to stand at -20°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
3	High temperature storage	The sample should be allowed to stand at 80°C for 120 hours under no-load condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
4	Low temperature storage	The sample should be allowed to stand at -30°C for 120 hours under no-load condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
5	Moisture storage	The sample should be allowed to stand at 60°C, 90%RH MAX for 240 hours under no-load condition , then taking it out and drying it at normal temperature for 2 hours.
6	Thermal shock storage	The sample should be allowed to stand the following 10 cycles: -30°C for 30 minutes->normal temperature for 5 minutes-> +80°C for 30 minutes -> normal temperature for 5 minutes, as one cycle.
7	Packing vibration	Frequency Range: 10Hz~55Hz Amplitude of vibration:1.5mm Sweep time:12min X,Y, Z 2 hours for each direction.
8	Packing drop	According to ASTM-D-5327
9	Electrical static discharge	Air: $\pm 4\text{kV}$ 150Pf/330 Ω 5 times Contact: $\pm 2\text{kV}$ 150Pf/330 Ω 5 times

*sample size for each test item is 3-5pcs

9.2 Testing Conditions and Inspection Criteria

For the final test, the testing sample must be stored at room temperature for 24 hours.

After the tests listed in table 9.2, standard specifications for reliability will be executed in order to ensure stability.

No.	Item	Test model	In section Criteria
1	Current Consumption	Refer to Specification	The current consumption should conform to the product specification
2	Contrast	Refer to Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
3	Appearance	Visual Inspection	Defect free

9.3 MTBF

MTG F	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50.000 hours under ordinary operating and storage conditions room temperature (25°C±5°C) ,normal humidity(50% ± 10% RH), and in area not exposed to direct sun light.
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10. LCD MODULES HANDLING PRECAUTIONS

- n** The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- n** If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If
- n** The substance come into contact with your skin or clothes promptly wash it off using soap and water.
- n** Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- n** The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- n** To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling the LCD module.
 - Tools required for assembly, such as soldering irons, must be properly grounded.
 - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- n** Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

11. OTHERS

- n** Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- n** If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- n** To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
 - Exposed area of the printed circuit board
 - Terminal electrode sections.