

DATA SHEET

LCD MODULE

**DEM 240320K2 VMX-PW-N
(A-TOUCH)**

2,8“ IPS-TFT

Product Specification

Ver.: 1

Revision History

Revision	Date	Originator	Detail	Remarks
0	26.08.2024	LL	Initial Release	-
1	01.09.2025	LL	Modify Module Parameter Modify Outline Drawing(B)	P4 P34

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs, a touch panel and a backlight unit. This TFT Module is having an universal interface pin layout to access all relevant interfaces of the TFT Driver IC. It allows to be flexible in case of TFT driver IC changes.

2. Module Parameter

Features	Details	Unit
Display Size (Diagonal)	2.8"	-
LCD Type	IPS TFT	-
Display Mode	Transmissive / Normally Black	-
Resolution	240 x RGB x 320	Pixels
View Direction	Full View	Best Image
Module Outline	50.00 x 69.20 x 3.70 (Note1)	mm
Active Area	43.20 x 57.60	mm
Pixel Size	0.180 x 0.180	mm
Pixel Arrangement	RGB Vertical Stripe	-
Polarizer Surface Treatment	Anti-Glare	-
Display Colors	262k	-
Interface	8/9/16/18 MPU Interface or 3-Wire 9-Bit Interface + RGB Interface or 4-Wire 8-Bit Interface + RGB Interface	-
With or without Touch Panel	With RTP	
Driver IC	ST7789T3 (Sitronix)	-
Operating Temperature	-30°C ~ +80°C	°C
Storage Temperature	-30°C ~ +80°C	°C
Weight	t.b.d.	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

V_{SS}=0V, Ta=25°C

Item		Symbol	Min.	Max.	Unit
Supply Voltage	Logic	IOVCC	-0.3	4.6	V
	Analog	VCC	-0.3	4.6	V
Storage Temperature		T _{STG}	-30	+80	°C
Operating Temperature		T _{OP}	-30	+80	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	Logic	IOVCC	1.65	1.8/2.8	3.3	V
	Analog	VCC	2.4	2.8	3.3	V
Logic Low Input Voltage		V _{IL}	GND	-	0.3*IOVCC	V
Logic High Input Voltage		V _{IH}	0.7*IOVCC	-	IOVCC	V
Logic Low Output Voltage		V _{OL}	GND	-	0.2*IOVCC	V
Logic High Output Voltage		V _{OH}	0.8*IOVCC	-	IOVCC	V
Current Consumption All White	Logic	I _{CC+} I _{IN}	-	TBD	-	mA
	Analog					

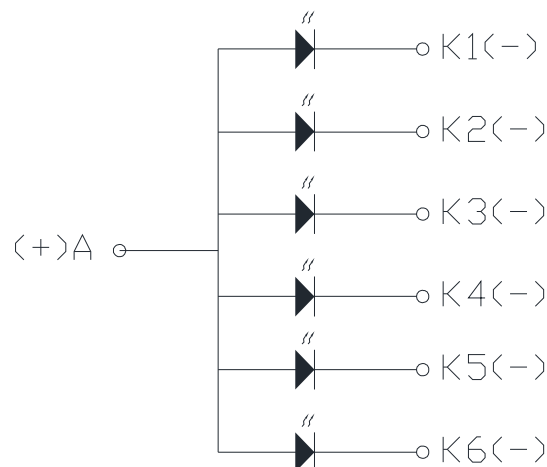
5. Backlight Characteristic

5.1. Backlight Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V _F	T _a =25 °C, I _F =20mA/LED	2.9	3.2	3.4	V
Forward Current	I _F	T _a =25 °C, V _F =3.2V/LED	-	120	-	mA
Power Dissipation	P _D		-	384	-	mW
LED Lifetime (25 °C)	-	-	-	50.000	-	Hr
Uniformity	Avg		-	80	-	%
Drive Method	Constant Current					
LED Configuration	6 White LEDs in parallel					

Note: LED lifetime defined as follows: The final brightness is at 50% of original brightness. The environmental conducted under ambient air flow, at T_a=25°C ± 2°C, 60%RH ± 5%, I_F=20mA/LED.

5.2. Backlighting Circuit



6. Touch Screen Panel Specifications**6.1 Electrical Characteristics**

Item	Min.	Typ.	Max.	Unit	Note
Linearity	-1.5	-	1.5	%	Analog X and Y directions
Terminal Resistance	100	-	550	Ω	X(Film side)
	300	-	850	Ω	Y(Glass side)
Insulation Resistance	20	-	-	M Ω	DC $\leq$ 10V
Voltage	-	5	10	V	DC
Chattering	-	-	10	ms	-

Caution (1): Do not operate it with a thing except a polyacetal pen (tip R0.8mm or less) or a finger, especially those with hard or sharp tips such as a ball point pen or a mechanical pencil.

6.2 Mechanical & Reliability Characteristics

Item	Min.	Typ.	Max.	Unit	Note
Activation Force	30	-	120	g	(1)
Durability-Surface Scratching	Write 20,000	-	-	characters	(2)
Durability-Surface Pitting	1,000,000	-	-	touches	(3)
Surface Hardness	3	-	-	H	-

Note (1) Stylus pen Input: R0.8mm polyacetal pen or Finger

Note (2) Measurement for Surface area

- Force: 150-250gf
- Speed: 60mm/sec
- Stylus: R0.8 polyacetal pen or Finger

Note (3) Pit 1,000,000 times on the Film with a R3.75 silicon rubber.

- Force: Force: 2.45N
- Speed: 3times/sec

7. Optical Characteristics

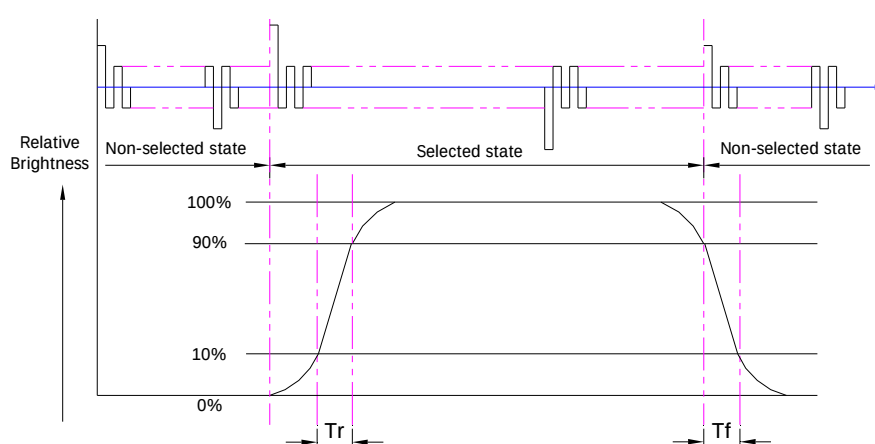
7.1. Optical Characteristics

Ta=25°C, VCC=2.8V

Backlight On (Transmissive Mode)	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
	Luminance on TFT(I_f =20mA/LED)		Lv	Normally viewing angle $\theta_x = \varphi_y =0^\circ$	440	550	-	cd/m ²
	Contrast Ratio(See 7.3)		CR		(1000)	(1200)	-	-
	Response Time (See 7.2)		TR+TF		-	30	35	ms
	Chromaticity Transmissive (See 7.5)	Red	XR		-	TBD	-	-
			YR		-	TBD	-	-
		Green	XG		-	TBD	-	-
			YG		-	TBD	-	-
		Blue	XB		-	TBD	-	-
			YB		-	TBD	-	-
		White	Xw		-	TBD	-	-
			Yw		-	TBD	-	-
	Viewing Angle (See 7.4)	Horizontal	θ_{x+}	Center CR≥10	75	80	-	Deg.
			θ_{x-}		75	80	-	
		Vertical	φ_{Y+}		75	80	-	
			φ_{Y-}		75	80	-	
		NTSC Ratio(Gamut)				65	70	-

7.2. Definition of Response Time

7.2.1. Normally Black Type (Negative)

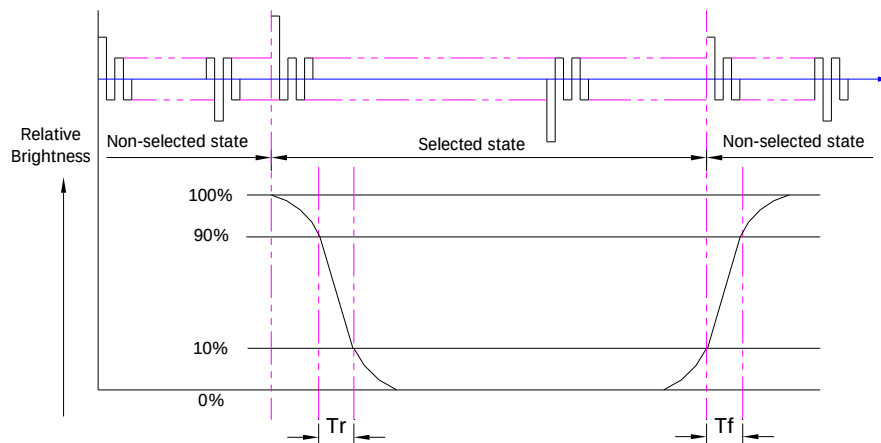


Tr is the time it takes to change from non-selected state with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note: Measuring machine: LCD-5100

7.2.2. Normally White Type (Positive)



T_r is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

T_f is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

7.3. Definition of Contrast Ratio

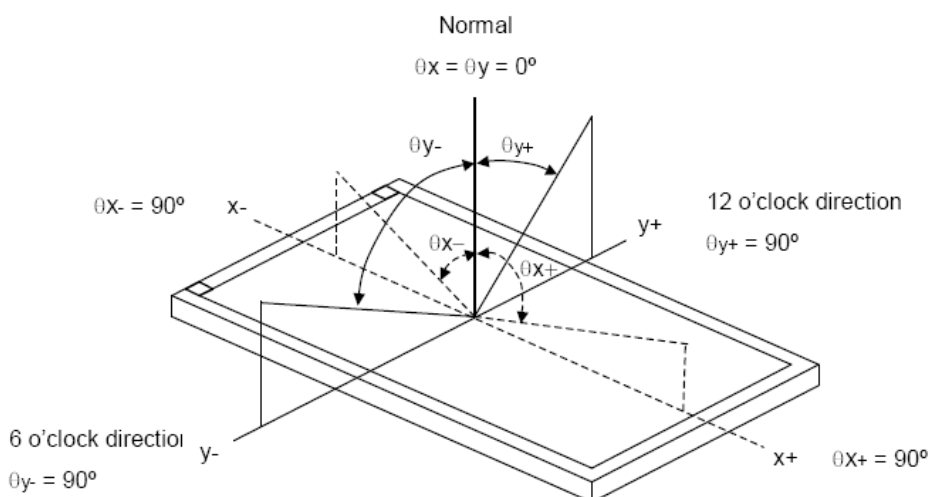
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

7.4. Definition of Viewing Angles



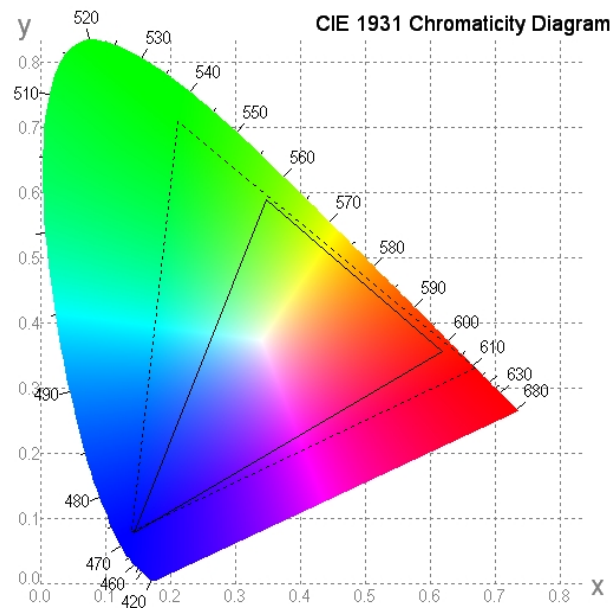
Measuring machine: LCD-5100 or EQUI

7.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



7.6. Definition of Surface Luminance, Uniformity and Transmittance

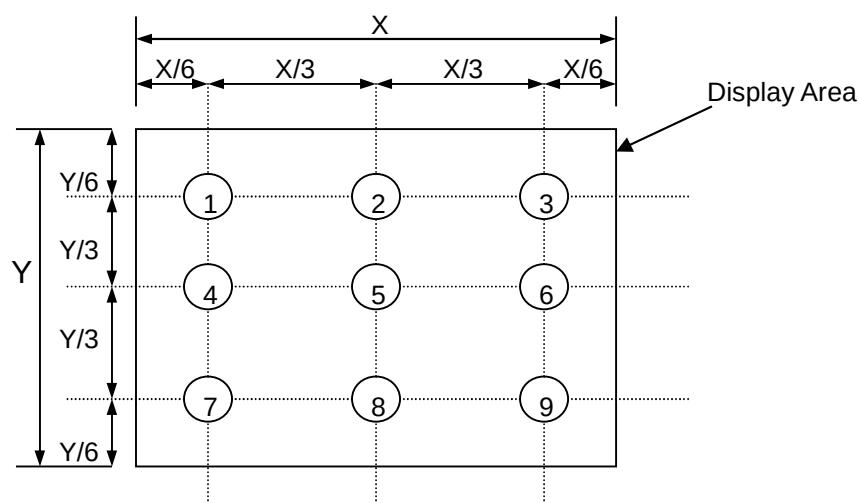
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

7.6.1. Surface Luminance: $L_v = \text{average } (L_{P1}:L_{P9})$

7.6.2. Uniformity = Minimal ($L_{P1}:L_{P9}$) / Maximal ($L_{P1}:L_{P9}$) * 100%

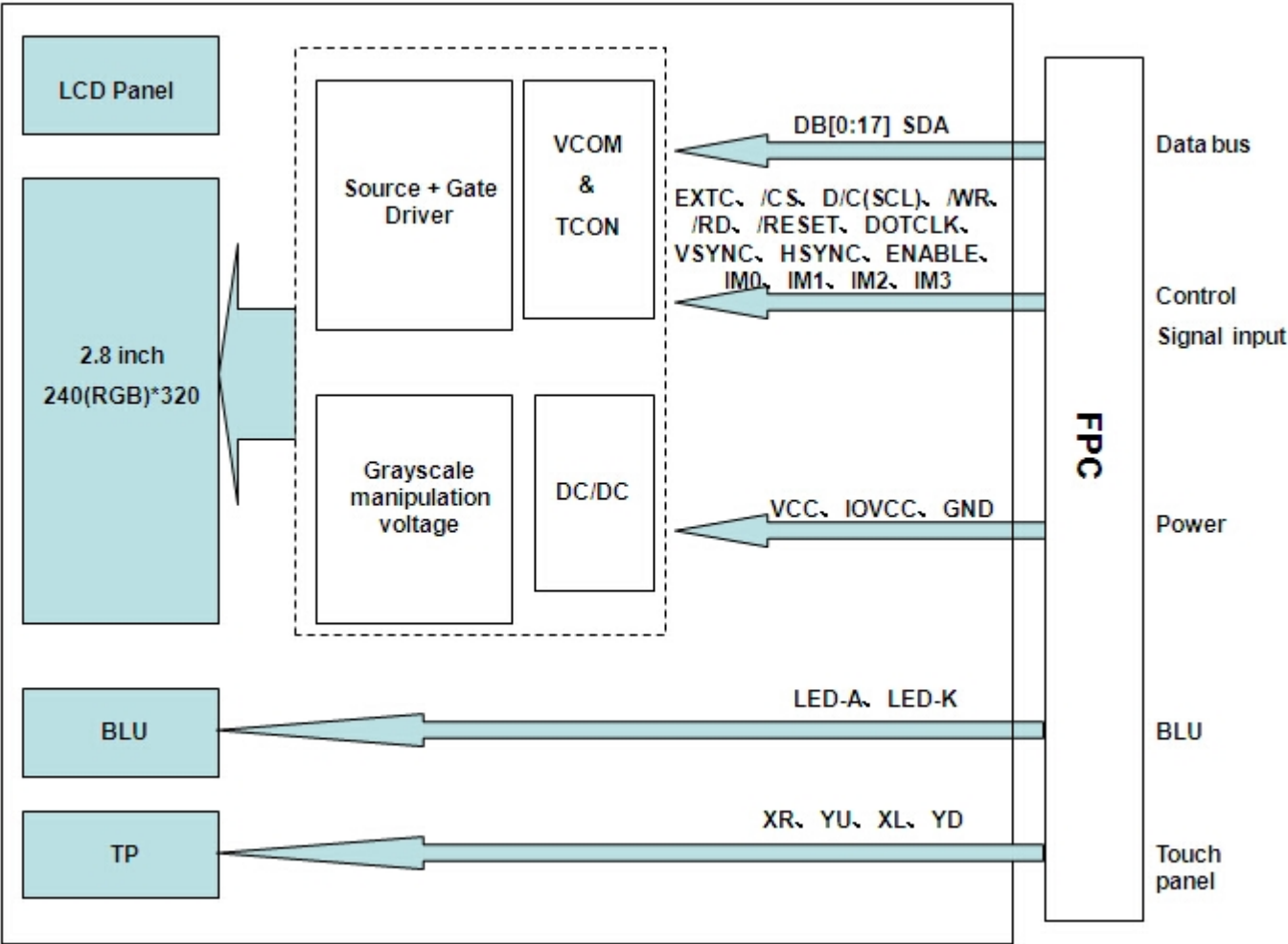
7.6.3. Transmittance = $L_v \text{ on LCD} / L_v \text{ on Backlight} * 100\%$

Note: Measuring machine: BM-7



Note: Test 3 brightness points below 3.5 inches(point2/5/8), and test 5 brightness points between 3.5-7 inches(point1/3/5/7/9). Test 9 brightness points above 7 inches(point1 to 9)

8. Block Diagram and Power Supply

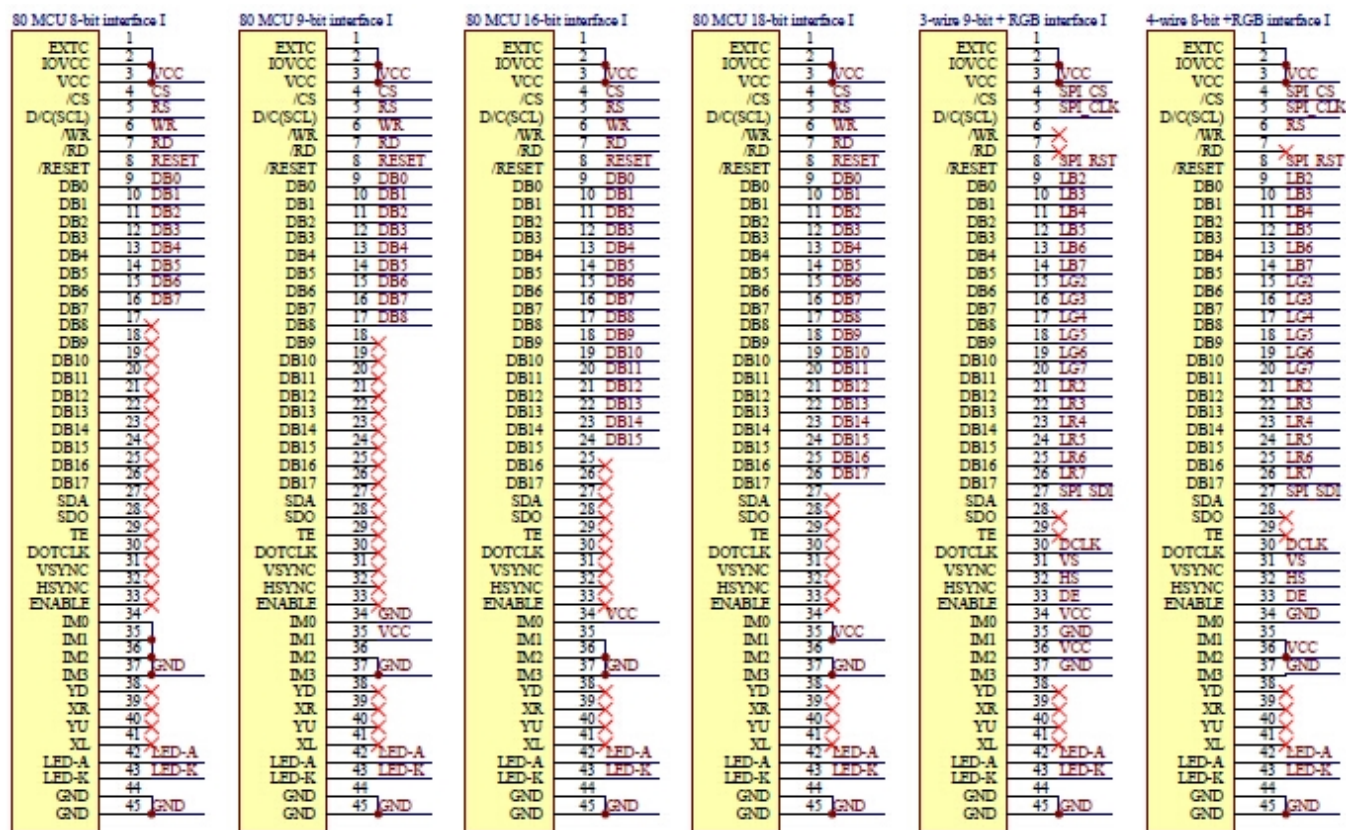


9. Interface Pins Definition

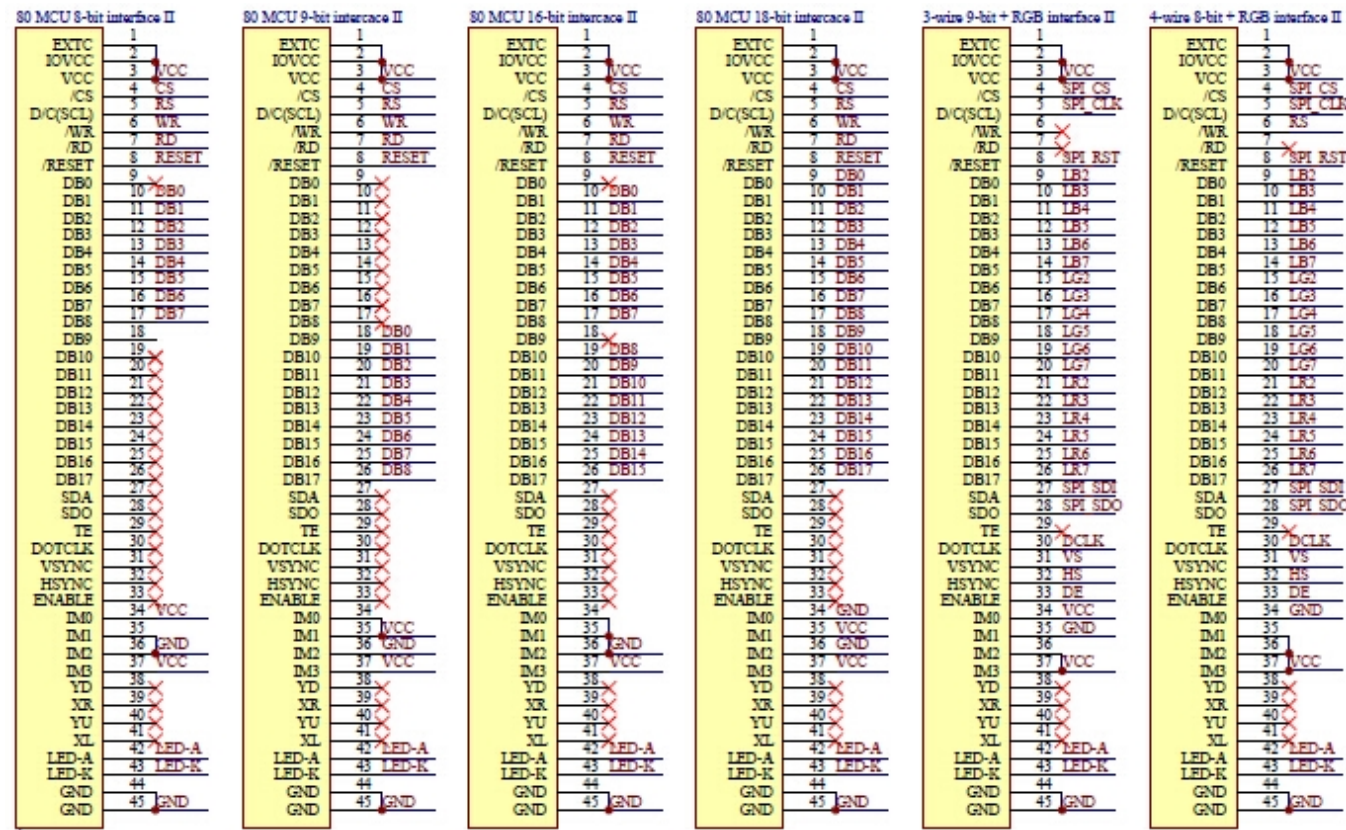
No.	Symbol	Function
1	EXTC	Extended command set enable. High: extended command set is accepted. EXTC must be set "H" to read/write extended registers.
2	IOVCC(1.8/2.8V)	Digital Supply Power.
3	VCC(2.8V)	Analog Supply Power.
4	/CS	Chip Select Signal (Low: active).
5	D/C(SCL)	D/C: Data/Commander Selection. SCL: Serial clock in 3-wire 9-bit/4-wire 8-bit serial data interface.
6	/WR	8080-system: Serves as Write Signal. 4-line system: Serves as the selector of command or parameter.
7	/RD	Read Signal.
8	/RESET	Reset Signal.
9	DB0	Data Bus.
10	DB1	Data Bus.
11	DB2	Data Bus.
12	DB3	Data Bus.
13	DB4	Data Bus.
14	DB5	Data Bus.
15	DB6	Data Bus.
16	DB7	Data Bus.
17	DB8	Data Bus.
18	DB9	Data Bus.
19	DB10	Data Bus.
20	DB11	Data Bus.
21	DB12	Data Bus.
22	DB13	Data Bus.
23	DB14	Data Bus.
24	DB15	Data Bus.
25	DB16	Data Bus.
26	DB17	Data Bus.
27	SDA	SPI interface input pin.
28	SDO	SPI interface output pin.
29	TE	Tearing effect output pin to synchronize MPU to frame writing, active by S/W command.
30	DOTCLK	Pixel clock signal in RGB I/F mode.
31	VSYNC	Vertical sync. Signal in RGB I/F mode.
32	HSYNC	Horizontal sync. Signal in RGB I/F mode.
33	ENABLE	Data enable signal in RGB I/F mode.

34-37	IM0 IM1 IM2 IM3							
		IM3	IM2	IM1	IM0	MPU interface Mode	DB pin	
							Register	Gram
		0	0	0	0	80 MCU 8-bit bus interface I	DB[7:0]	DB[7:0]
		0	0	0	1	80 MCU 16-bit bus interface I	DB[7:0]	DB[15:0]
		0	0	1	0	80 MCU 9-bit bus interface I	DB[7:0]	DB[8:0]
		0	0	1	1	80 MCU 18-bit bus interface I	DB[7:0]	DB[17:0]
		0	1	0	1	3-wire 9-bit data Serial interface I	SDA: in/out	
		0	1	1	0	4-wire 8-bit data Serial interface I	SDA: in/out	
		1	0	0	0	80 MCU 16-bit bus interface II	DB[8:1]	DB[8:1] DB[17:10]
		1	0	0	1	80 MCU 8-bit bus interface II	DB[17:10]	DB[17:10]
		1	0	1	0	80 MCU 18-bit bus interface II	DB[8:1]	DB[17:0]
		1	0	1	1	80 MCU 9-bit bus interface II	DB[17:10]	DB[17:9]
		1	1	0	1	3-wire 9-bit data Serial interface II	SDI: in SDO: out	
		1	1	1	0	4-wire 8-bit data Serial interface II	SDI: in SDO: out	
38	YD	Touch panel terminal.						
39	XR	Touch panel terminal.						
40	YU	Touch panel terminal.						
41	XL	Touch panel terminal.						
42	LED-A	LED Anode.						
43	LED-K	LED Cathode.						
44	GND	Ground.						
45	GND	Ground.						

80 MCU Interface I

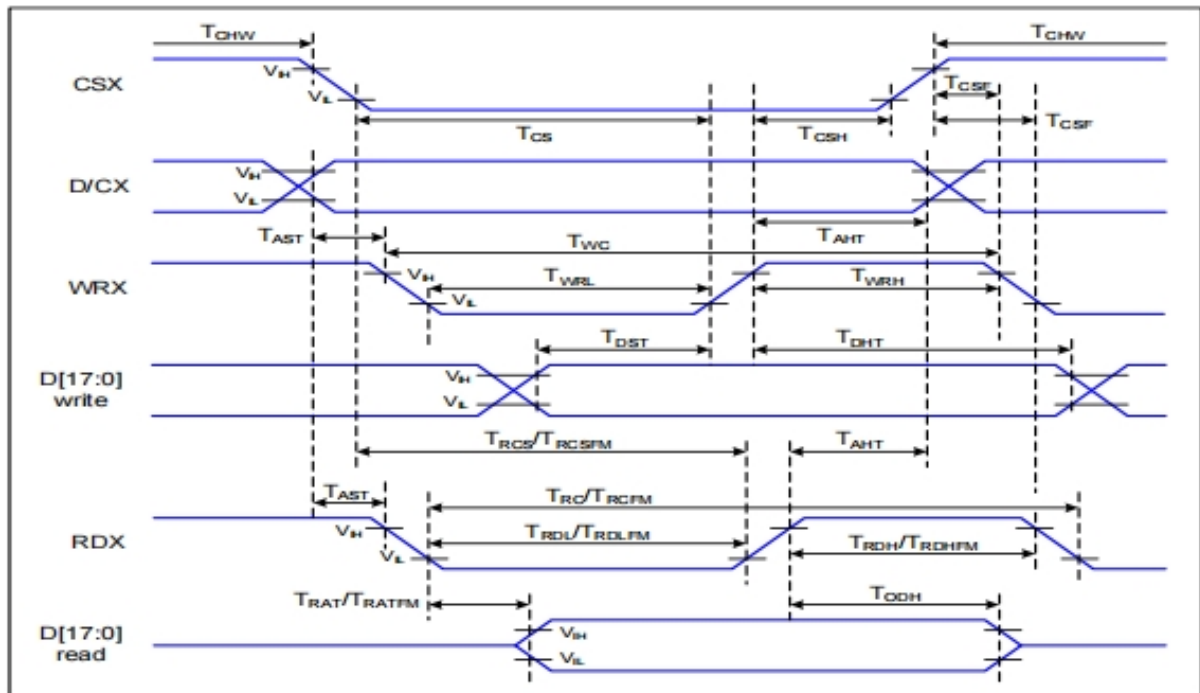


80 MCU Interface II



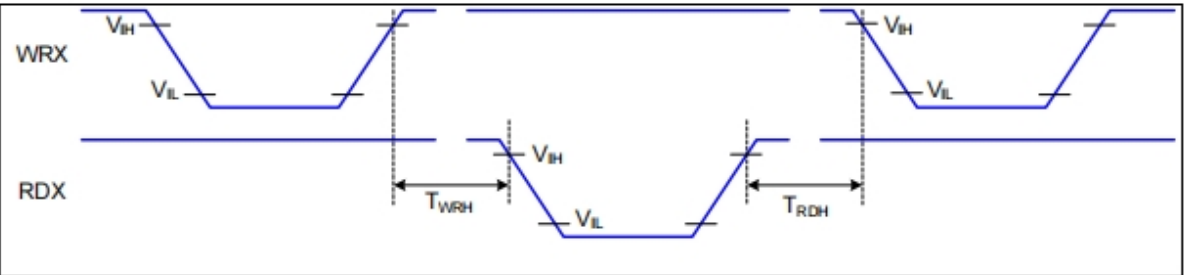
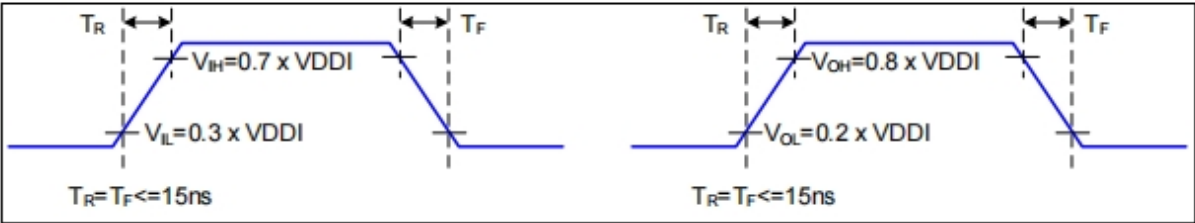
10. AC Characteristics

9.1 Display Parallel 18/16/9/8-Bit Interface Timing Characteristics

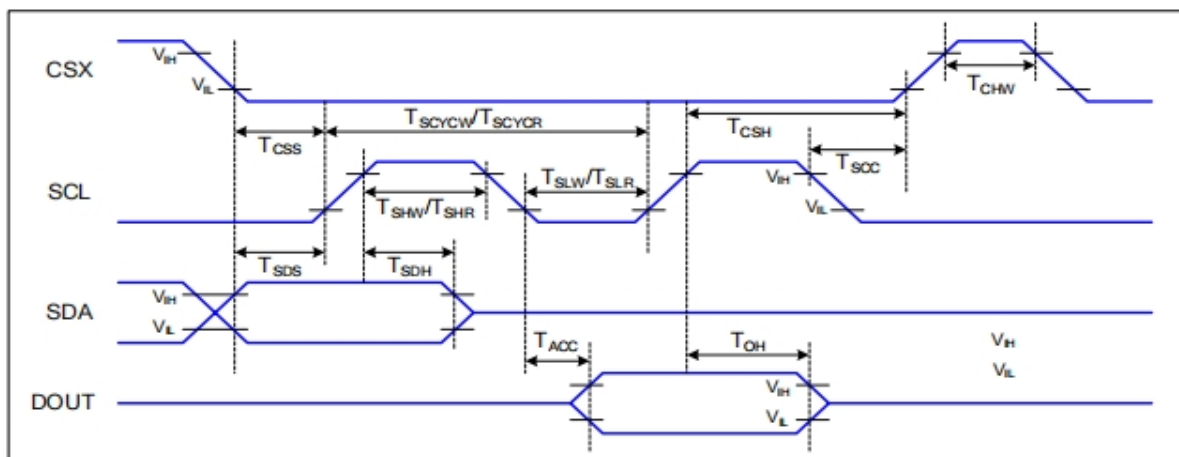


Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	-
	T _{AHT}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	-
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RC}	Chip select setup time (Read ID)	45		ns	
	T _{RCFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
WRX	T _{WC}	Write cycle	66		ns	
	T _{WRH}	Control pulse "H" duration	15		ns	
	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RDH}	Control pulse "H" duration (ID)	90		ns	
	T _{RDL}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	
	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	
D[17:0]	T _{DST}	Data setup time	10		ns	For CL=30pF

	T _{DHT}	Data hold time	10		ns
	T _{RAT}	Read access time (ID)		40	ns
	T _{RATFM}	Read access time (FM)		340	ns
	T _{ODH}	Output disable time	20	80	ns



9.3 Display Serial Interface Timing Characteristics (3-Line SPI System)

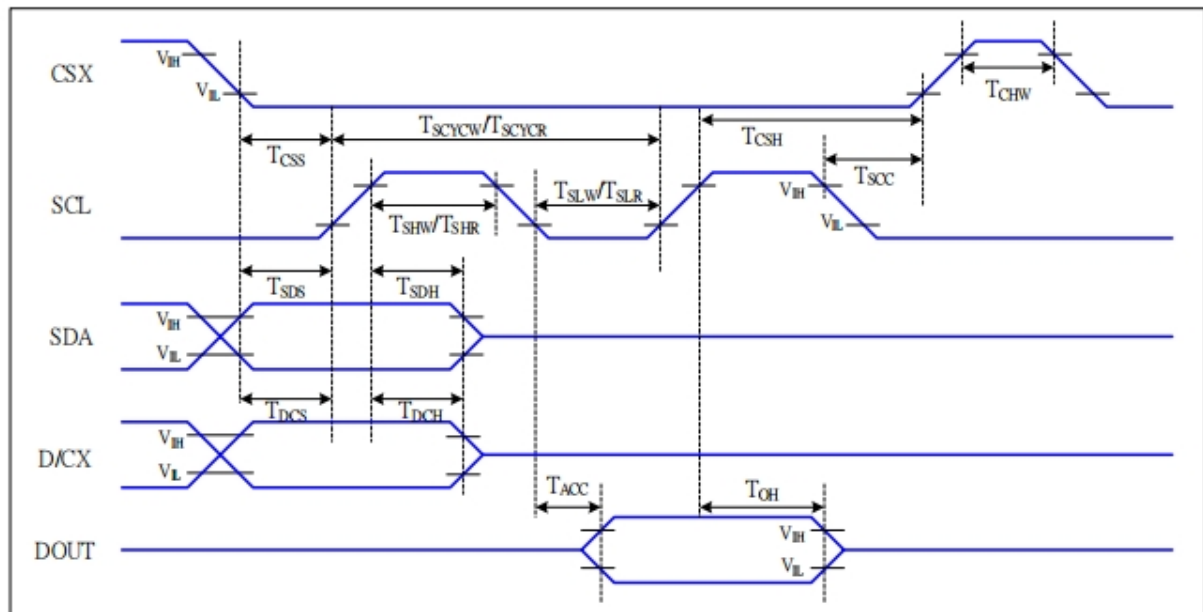


Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	65		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	16		ns	
	T_{SHW}	SCL "H" pulse width (Write)	7		ns	
	T_{SLW}	SCL "L" pulse width (Write)	7		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T_{SDS}	Data setup time	7		ns	
	T_{SDH}	Data hold time	7		ns	
DOUT	T_{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T_{OH}	Output disable time	15	50	ns	For minimum CL=8pF

Note1 : The rising time and falling time (T_r , T_f) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals

Note2: In the read sequence of serial interface, the 500nsec delay time is needed between read command and first read clock..

9.4 Display Serial Interface Timing Characteristics (4-Line SPI System)

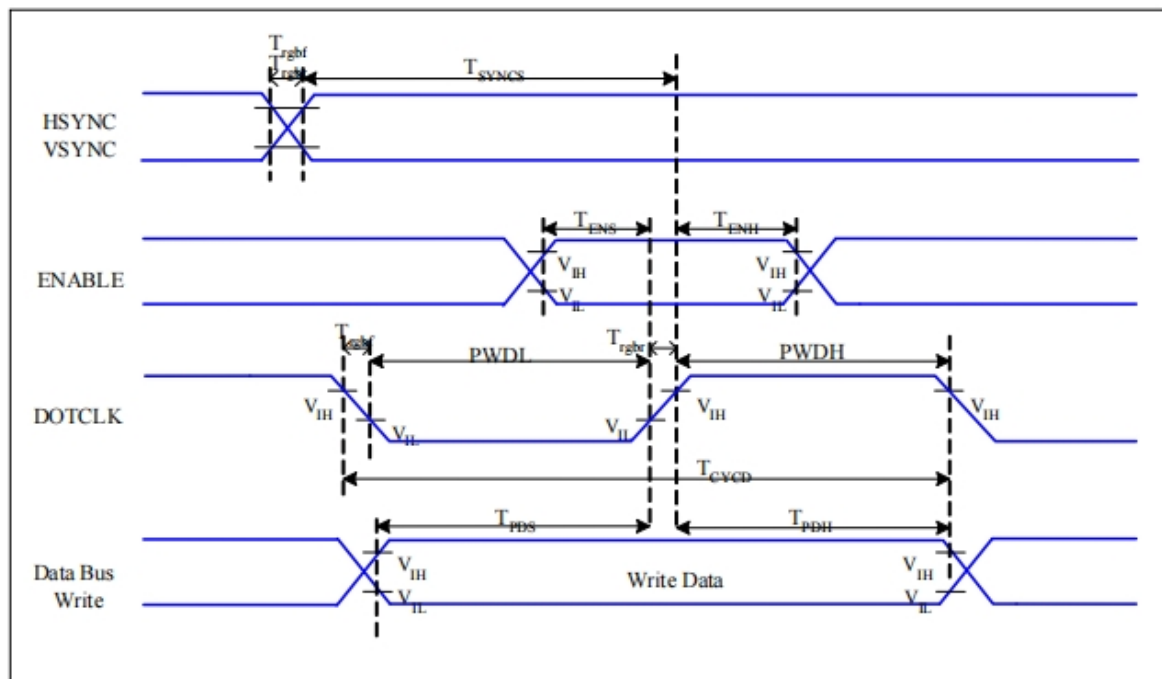


Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	65		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	16		ns	-write command & data ram
	T_{SHW}	SCL "H" pulse width (Write)	7		ns	
	T_{SLW}	SCL "L" pulse width (Write)	7		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
D/CX	T_{DCS}	D/CX setup time	10		ns	
	T_{DCH}	D/CX hold time	10		ns	
SDA (DIN)	T_{SDS}	Data setup time	7		ns	
	T_{SDH}	Data hold time	7		ns	
DOUT	T_{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T_{OH}	Output disable time	15	50	ns	For minimum CL=8pF

Note1 : The rising time and falling time (T_r , T_f) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals

Note2: In the read sequence of serial interface, the 500nsec delay time is needed between read command and first read clock.

9.5 RGB Interface Timing Characteristics



Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYNC, HSYNC Setup Time	30	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	25	-	ns	
	T_{ENH}	Enable Hold Time	25	-	ns	
DOTCLK	$PWDH$	DOTCLK High-level Pulse Width	60	-	ns	
	$PWDL$	DOTCLK Low-level Pulse Width	60	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	120	-	ns	
	$T_{\text{rghr}}, T_{\text{rghf}}$	DOTCLK Rise/Fall time	-	20	ns	
DB	T_{PDS}	PD Data Setup Time	50	-	ns	
	T_{PDH}	PD Data Hold Time	50	-	ns	

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYNC, HSYNC Setup Time	35	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	35	-	ns	

	T_{ENH}	Enable Hold Time	35	-	ns	
DOTCLK	$PWDH$	DOTCLK High-level Pulse Width	35	-	ns	
	$PWDL$	DOTCLK Low-level Pulse Width	35	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	80	-	ns	
	$T_{\text{rghr}}, T_{\text{rghf}}$	DOTCLK Rise/Fall time	-	10	ns	
DB	T_{PDS}	PD Data Setup Time	35	-	ns	
	T_{PDH}	PD Data Hold Time	35	-	ns	

11. Command Table

Instruction	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
NOP	0	↑	1	-	0	0	0	0	0	0	0	0	(00h)	No operation
SWRESET	0	↑	1	-	0	0	0	0	0	0	0	1	(01h)	Software reset
RDDID	0	↑	1	-	0	0	0	0	0	1	0	0	(04h)	Read display ID
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	ID17	ID16	ID15	ID14	ID13	ID12	ID11	ID10		ID1 read
	1	1	↑	-	ID27	ID26	ID25	ID24	ID23	ID22	ID21	ID20		ID2 read
	1	1	↑	-	ID37	ID36	ID35	ID34	ID33	ID32	ID31	ID30		ID3 read
RDDST	0	↑	1	-	0	0	0	0	1	0	0	1	(09h)	Read display status
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	BSTON	MY	MX	MV	ML	RGB	MH	ST24		-
	1	1	↑	-	ST23	IFPF2	IFPF1	IFPF0	IDMON	PTLON	SLOUT	NORON		-
	1	1	↑	-	ST15	ST14	INVON	ST12	ST11	DISON	TEON	GCS2		-
	1	1	↑	-	GCS1	GCS0	TEM	ST4	ST3	ST2	ST1	ST0		-
RDDPM	0	↑	1	-	0	0	0	0	1	0	1	0	(0Ah)	Read display power
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	BSTON	IDMON	PTLON	SLPOUT	NORON	DISON	0	0		
RDD MADCTL	0	↑	1	-	0	0	0	0	1	0	1	1	(0Bh)	Read display
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	MY	MX	MV	ML	RGB	MH	0	0		-
RDD COLMOD	0	↑	1	-	0	0	0	0	1	1	0	0	(0Ch)	Read display pixel
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	0	D6	D5	D4	0	D2	D1	D0		-
RDDIM	0	↑	1	-	0	0	0	0	1	1	0	1	(0Dh)	Read display image
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	VSSON	0	INVON	0	0	GC2	GC1	GC0		-
RDDSM	0	↑	1	-	0	0	0	0	1	1	1	0	(0Eh)	Read display signal
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read

Instruction	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
	1	1	↑	-	TEON	TEM	0	0	0	0	0	0		-
RDDSDR	0	↑	1	-	0	0	0	0	1	1	1	1	(0Fh)	Read display self-diagnostic result
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	D7	D6	0	0	0	0	0	0		-
SLPIN	0	↑	1	-	0	0	0	1	0	0	0	0	(10h)	Sleep in
SLPOUT	0	↑	1	-	0	0	0	1	0	0	0	1	(11h)	Sleep out
PTLON	0	↑	1	-	0	0	0	1	0	0	1	0	(12h)	Partial mode on
NORON	0	↑	1	-	0	0	0	1	0	0	1	1	(13h)	Partial off (Normal)
INVOFF	0	↑	1	-	0	0	1	0	0	0	0	0	(20h)	Display inversion off
INVON	0	↑	1	-	0	0	1	0	0	0	0	1	(21h)	Display inversion on
GAMSET	0	↑	1	-	0	0	1	0	0	0	0	1	(26h)	Display inversion
	1	↑	1	-	0	0	0	0	GC3	GC2	GC1	GC0		on
DISPOFF	0	↑	1	-	0	0	1	0	1	0	0	0	(28h)	Display off
DISPON	0	↑	1	-	0	0	1	0	1	0	0	1	(29h)	Display on
CASET	0	↑	1	-	0	0	1	0	1	0	1	0	(2Ah)	Column address set
	1	↑	1	-	XS15	XS14	XS13	XS12	XS11	XS10	XS9	XS8		X address start:
	1	↑	1		XS7	XS6	XS5	XS4	XS3	XS2	XS1	XS0		$0 \leq XS \leq X$
	1	↑	1		XE15	XE14	XE13	XE12	XE11	XE10	XE9	XE8		X address start:
	1	↑	1		XE7	XE6	XE5	XE4	XE3	XE2	XE1	XE0		$S \leq XE \leq X$
RASET	0	↑	1	-	0	0	1	0	1	0	1	1	(2Bh)	Row address set
	1	↑	1	-	YS15	YS14	YS13	YS12	YS11	YS10	YS9	YS8		Y address start:
	1	↑	1		YS7	YS6	YS5	YS4	YS3	YS2	YS1	YS0		$0 \leq YS \leq Y$
	1	↑	1		YE15	YE14	YE13	YE12	YE11	YE10	YE9	YE8		Y address start:
	1	↑	1		YE7	YE6	YE5	YE4	YE3	YE2	YE1	YE0		$S \leq YE \leq Y$
RAMWR	0	↑	1	-	0	0	1	0	1	1	0	0	(2Ch)	Memory write
	1	↑	1	D1[17:8]	D1[7]	D1[6]	D1[5]	D1[4]	D1[3]	D1[2]	D1[1]	D1[0]		Write data
	1	↑	1	Dx[17:8]	Dx[7]	Dx[6]	Dx[5]	Dx[4]	Dx[3]	Dx[2]	Dx[1]	Dx[0]		
	1	↑	1	Dn[17:8]	Dn[7]	Dn[6]	Dn[5]	Dn[4]	Dn[3]	Dn[2]	Dn[1]	Dn[0]		
RAMRD	0	↑	1	-	0	0	1	0	1	1	1	0	(2Eh)	Memory read

Instruction	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	D1[17:8]	D1[7]	D1[6]	D1[5]	D1[4]	D1[3]	D1[2]	D1[1]	D1[0]		Read data
	1	1	↑	Dx[17:8]	Dx[7]	Dx[6]	Dx[5]	Dx[4]	Dx[3]	Dx[2]	Dx[1]	Dx[0]		
	1	1	↑	Dn[17:8]	Dn[7]	Dn[6]	Dn[5]	Dn[4]	Dn[3]	Dn[2]	Dn[1]	Dn[0]		
PTLAR	0	↑	1	-	0	0	1	1	0	0	0	0	(30h)	Partial start/end address set
	1	↑	1	-	PSL15	PSL14	PSL13	PSL12	PSL11	PSL10	PSL9	PSL8		Partial start address: (0, 1, 2, ...P)
	1	↑	1	-	PSL7	PSL6	PSL5	PSL4	PSL3	PSL2	PSL1	PSL0		
	1	↑	1	-	PEL15	PEL14	PEL13	PEL12	PEL11	PEL10	PEL9	PEL8		Partial end address (0, 1, 2, ...3, ...P)
	1	↑	1	-	PEL7	PEL6	PEL5	PEL4	PEL3	PEL2	PEL1	PEL0		
VSCRDEF	0	↑	1	-	0	0	1	1	0	0	1	1	(33h)	Vertical scrolling definition
	1	↑	1	-	TFA15	TFA14	TFA13	TFA12	TFA11	TFA10	TFA9	TFA8		
	1	↑	1	-	TFA7	TFA6	TFA5	TFA4	TFA3	TFA2	TFA1	TFA0		
	1	↑	1	-	VSA15	VSA14	VSA13	VSA12	VSA11	VSA10	VSA9	VSA8		
	1	↑	1	-	VSA7	VSA6	VSA5	VSA4	VSA3	VSA2	VSA1	VSA0		
	1	↑	1	-	BFA15	BFA14	BFA13	BFA12	BFA11	BFA10	BFA9	BFA8		
	1	↑	1	-	BFA7	BFA6	BFA5	BFA4	BFA3	BFA2	BFA1	BFA0		
TEOFF	0	↑	1	-	0	0	1	1	0	1	0	0	(34h)	Tearing effect line off
TEON	0	↑	1	-	0	0	1	1	0	1	0	1	(35h)	Tearing effect line on
	1	↑	1	-	-	-	-	-	-	-	-	TEM		
MADCTL	0	↑	1	-	0	0	1	1	0	1	1	0	(36h)	Memory data access control
	1	↑	1	-	MY	MX	MV	ML	RGB	0	0	0		-
VSCRSADD	0	↑	1	-	0	0	1	1	0	1	1	1	(37h)	Vertical scrolling start address
	1	↑	1	-	VSP15	VSP14	VSP13	VSP12	VSP11	VSP10	VSP9	VSP8		
	1	↑	1	-	VSP7	VSP6	VSP5	VSP4	VSP3	VSP2	VSP1	VSP0		
IDMOFF	0	↑	1	-	0	0	1	1	1	0	0	0	(38h)	Idle mode off
IDMON	0	↑	1	-	0	0	1	1	1	0	0	1	(39h)	Idle mode on

Instruction	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
COLMOD	0	↑	1	-	0	0	1	1	1	0	1	0	(3Ah)	Interface pixel format
	1	↑	1	-	0	D6	D5	D4	0	D2	D1	D0		Interface format
RAMWRC	0	↑	1	-	0	0	1	1	1	1	0	0	(3Ch)	Memory write continue
	1	↑	1	D1[17:8]	D1[7]	D1[6]	D1[5]	D1[4]	D1[3]	D1[2]	D1[1]	D1[0]		Write data
	1	↑	1	Dx[17:8]	Dx[7]	Dx[6]	Dx[5]	Dx[4]	Dx[3]	Dx[2]	Dx[1]	Dx[0]		
	1	↑	1	Dn[17:8]	Dn[7]	Dn[6]	Dn[5]	Dn[4]	Dn[3]	Dn[2]	Dn[1]	Dn[0]		
RAMRDC	0	↑	1	-	0	0	1	1	1	1	1	0	(3Eh)	Memory read continue
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy Read
	1	1	↑	D1[17:8]	D1[7]	D1[6]	D1[5]	D1[4]	D1[3]	D1[2]	D1[1]	D1[0]		
	1	1	↑	Dx[17:8]	Dx[7]	Dx[6]	Dx[5]	Dx[4]	Dx[3]	Dx[2]	Dx[1]	Dx[0]		
	1	1	↑	Dn[17:8]	Dn[7]	Dn[6]	Dn[5]	Dn[4]	Dn[3]	Dn[2]	Dn[1]	Dn[0]		
TESCAN	0	↑	1	-	0	1	0	0	0	1	0	0	(44h)	Set tear scanline
	1	↑	1	-	N15	N14	N13	N12	N11	N10	N9	N8		
	1	↑	1	-	N7	N6	N5	N4	N3	N2	N1	N0		
RDTESCAN	0	↑	1	-	0	1	0	0	0	1	0	1	(45h)	Get scanline
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy Read
	1	1	↑	-	-	-	-	-	-	-	N9	N8		
	1	1	↑	-	N7	N6	N5	N4	N3	N2	N1	N0		
WRDISBV	0	↑	1	-	0	1	0	1	0	0	0	1	(51h)	Write display brightness
	1	↑	1	-	DBV7	DBV6	DBV5	DBV4	DBV3	DBV2	DBV1	DBV0		
RDDISBV	0	↑	1	-	0	1	0	1	0	0	1	0	(52h)	Read display brightness value
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	DBV7	DBV6	DBV5	DBV4	DBV3	DBV2	DBV1	DBV0		
WRCTRLD	0	↑	1	-	0	1	0	1	0	0	1	1	(53h)	Write CTRL display
	1	↑	1	-	0	0	BCTRL	0	DD	BL	0	0		
RDCTRLD	0	↑	1	-	0	1	0	1	0	1	0	0	(54h)	Read CTRL value display
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	0	0	BCTRL	0	DD	BL	0	0		

Instruction	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
WRCACE	0	↑	1	-	0	1	0	1	0	1	0	1	(59h)	Write content adaptive brightness control and Color enhancemnet
	1	↑	1	-	CECTRL	0	CE1	CE0	0	0	C1	C0		
RDCABC	0	↑	1	-	0	1	0	1	0	1	1	0	(56h)	Read content adaptive brightness control
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	0	CECTRL	0	0	0	0	C1	C0		
WRCABCMB	0	↑	1	-	0	1	0	1	1	1	1	0	(5Eh)	Write CABC minimum brightness
	1	↑	1	-	CMB7	CMB6	CMB5	CMB4	CMB3	CMB2	CMB1	CMB0		
RDCABCMB	0	↑	1	-	0	1	0	1	1	1	1	1	(5Fh)	Read CABC minimum brightness
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	CMB7	CMB6	CMB5	CMB4	CMB3	CMB2	CMB1	CMB0		
RDABCSDR	0	↑	1	-	0	1	1	0	1	0	0	0	(68h)	Read Automatic Brightness Control Self-Diagnostic Result
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	D7	D6	0	0	0	0	0	0		-
RDID1	0	↑	1	-	1	1	0	1	1	0	1	0	(DAh)	Read ID1
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	ID17	ID16	ID15	ID14	ID13	ID12	ID11	ID10		Read parameter
RDID2	0	↑	1	-	1	1	0	1	1	0	1	1	(DBh)	Read ID2
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑	-	ID27	ID26	ID25	ID24	ID23	ID22	ID21	ID20		Read parameter
RDID3	0	↑	1	-	1	1	0	1	1	1	0	0	(DCh)	Read ID3

Instruction	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
	1	1	↑	-	-	-	-	-	-	-	-	-		Dummy read
	1	1	↑		ID37	ID36	ID35	ID34	ID33	ID32	ID31	ID30		Read parameter

12. Quality Assurance

12.1.Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

12.2.Standard for Quality Test

12.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

12.2.2. Sampling Criteria:

Visual inspection: AQL 1.5.

Electrical functional: AQL 0.65.

12.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

12.3.Nonconforming Analysis & Disposition

12.3.1. Nonconforming analysis:

12.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

12.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

12.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

12.3.2. Disposition of nonconforming:

12.3.2.1. Non-conforming product over PPM level will be replaced.

12.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

12.4.Agreement Items

Shall negotiate with customer if the following situation occurs:

12.4.1. There is any discrepancy in standard of quality assurance.

12.4.2. Additional requirement to be added in product specification.

12.4.3. Any other special problem.

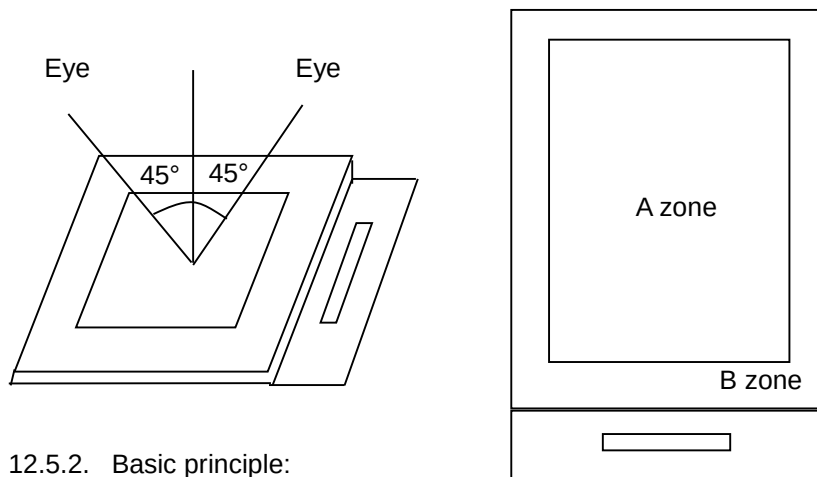
12.5.Standard of the Product Visual Inspection

12.5.1. Appearance inspection:

12.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

12.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

12.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,

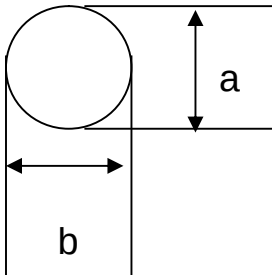


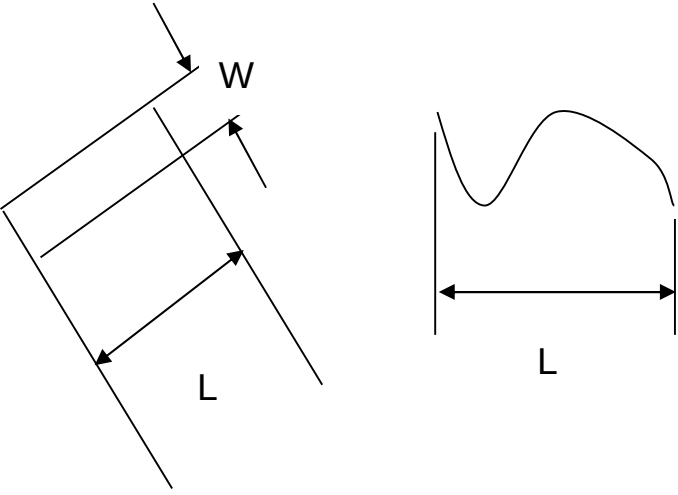
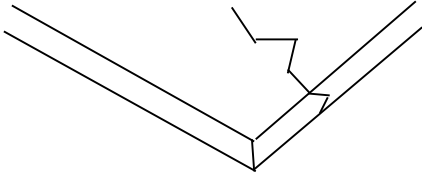
12.5.2. Basic principle:

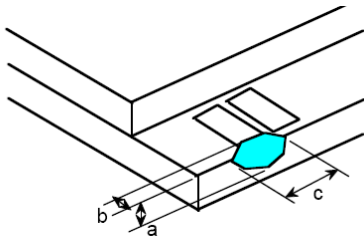
12.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

12.5.2.2. New item must be added on time when it is necessary.

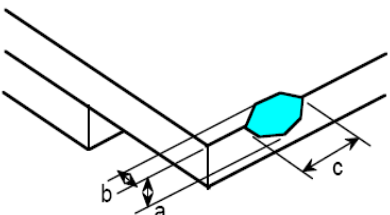
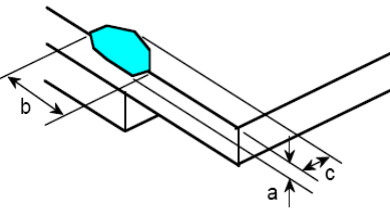
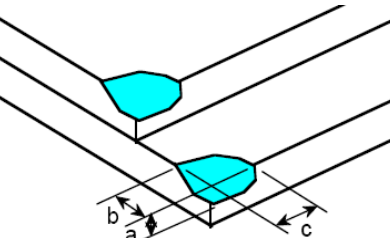
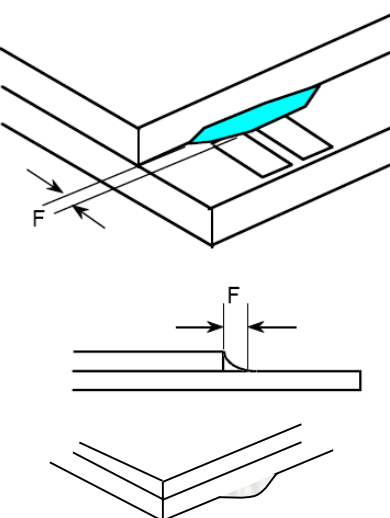
12.6. Inspection Specification

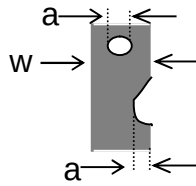
No.	Item	Criteria (Unit: mm)																		
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 $\varphi = (a + b) / 2$	<table><tr><th>Size \ Area</th><th>Acc. Qty</th></tr><tr><td>$\varphi \leq 0.10$</td><td>Ignore</td></tr><tr><td>$0.10 < \varphi \leq 0.15$</td><td>2</td></tr><tr><td>$0.15 < \varphi \leq 0.25$</td><td>1</td></tr><tr><td>$0.25 < \varphi$</td><td>0</td></tr><tr><td>Total</td><td>2 no include $\varphi \leq 0.10$</td></tr></table>	Size \ Area	Acc. Qty	$\varphi \leq 0.10$	Ignore	$0.10 < \varphi \leq 0.15$	2	$0.15 < \varphi \leq 0.25$	1	$0.25 < \varphi$	0	Total	2 no include $\varphi \leq 0.10$					
		Size \ Area	Acc. Qty																	
$\varphi \leq 0.10$	Ignore																			
$0.10 < \varphi \leq 0.15$	2																			
$0.15 < \varphi \leq 0.25$	1																			
$0.25 < \varphi$	0																			
Total	2 no include $\varphi \leq 0.10$																			
		Distance between 2 defects should more than 3mm apart.																		
02	Electrical Defect (Minor defect)	<table><tr><th></th><th>Display Area</th><th>Total</th><th rowspan="4">Note1</th></tr><tr><td>Bright dot</td><td>0</td><td>0</td></tr><tr><td>Dark dot</td><td>$N \leq 2$</td><td>$N \leq 2$</td></tr><tr><td>Total dot</td><td>$N \leq 2$</td><td>$N \leq 2$</td></tr><tr><td>Mura</td><td colspan="2">Not visible through 5% ND filters.</td><td>Note 2</td></tr></table>			Display Area	Total	Note1	Bright dot	0	0	Dark dot	$N \leq 2$	$N \leq 2$	Total dot	$N \leq 2$	$N \leq 2$	Mura	Not visible through 5% ND filters.		Note 2
			Display Area	Total	Note1															
Bright dot	0	0																		
Dark dot	$N \leq 2$	$N \leq 2$																		
Total dot	$N \leq 2$	$N \leq 2$																		
Mura	Not visible through 5% ND filters.		Note 2																	
		Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.																		

03	Black and White line Scratch Foreign material (Line type) (Minor defect)	 <table border="1" data-bbox="614 721 1241 1030"> <thead> <tr> <th>Length</th><th>Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td>/</td><td>$W \leq 0.03$</td><td>Ignore</td></tr> <tr> <td>$L \leq 2.5$</td><td>$0.03 < W \leq 0.05$</td><td>3</td></tr> <tr> <td>$L \leq 2.5$</td><td>$0.05 < W \leq 0.10$</td><td>2</td></tr> <tr> <td>/</td><td>$0.1 < W$</td><td>0</td></tr> <tr> <td colspan="2">Total</td><td>3</td></tr> </tbody> </table> Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.	Length	Width	Acc. Qty	/	$W \leq 0.03$	Ignore	$L \leq 2.5$	$0.03 < W \leq 0.05$	3	$L \leq 2.5$	$0.05 < W \leq 0.10$	2	/	$0.1 < W$	0	Total		3
Length	Width	Acc. Qty																		
/	$W \leq 0.03$	Ignore																		
$L \leq 2.5$	$0.03 < W \leq 0.05$	3																		
$L \leq 2.5$	$0.05 < W \leq 0.10$	2																		
/	$0.1 < W$	0																		
Total		3																		
04	Glass Crack (Minor defect)	 Crack is potential to enlarge, any type is not allowed.																		

05	Glass Chipping Pad Area: (Minor defect) 	

Length and Width	Acc. Qty
$c > 3.0, b < 1.0$	1
$c < 3.0, b < 1.0$	3
$a < \text{Glass Thickness}$	

06	Glass Chipping Rear of Pad Area: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>2</td></tr><tr><td>$c < 3.0, b < 0.5$</td><td>4</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
07	Glass Chipping Except Pad Area: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>2</td></tr><tr><td>$c < 3.0, b < 0.5$</td><td>4</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
08	Glass Corner Chipping: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c < 3.0, b < 3.0$</td><td>Ignore</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
09	Glass Burr: (Minor defect) 	<table><tr><th>Length</th><th>Acc. Qty</th></tr><tr><td>$F < 1.0$</td><td>Ignore</td></tr></table> Glass burr don't affect assemble and module dimension.	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	FPC Defect: (Minor defect) 	10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.										
11	Bubble on Polarizer (Minor defect)	<table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>$\varphi \leq 0.20$</td><td>Ignore</td></tr><tr><td>$0.20 < \varphi \leq 0.30$</td><td>4</td></tr><tr><td>$0.30 < \varphi \leq 0.50$</td><td>1</td></tr><tr><td>$0.50 < \varphi$</td><td>None</td></tr></table>	Diameter	Acc. Qty	$\varphi \leq 0.20$	Ignore	$0.20 < \varphi \leq 0.30$	4	$0.30 < \varphi \leq 0.50$	1	$0.50 < \varphi$	None
Diameter	Acc. Qty											
$\varphi \leq 0.20$	Ignore											
$0.20 < \varphi \leq 0.30$	4											
$0.30 < \varphi \leq 0.50$	1											
$0.50 < \varphi$	None											
12	Dent on Polarizer (Minor defect)	<table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>$\varphi \leq 0.20$</td><td>Ignore</td></tr><tr><td>$0.20 < \varphi \leq 0.30$</td><td>4</td></tr><tr><td>$0.30 < \varphi \leq 0.50$</td><td>1</td></tr><tr><td>$0.50 < \varphi$</td><td>None</td></tr></table>	Diameter	Acc. Qty	$\varphi \leq 0.20$	Ignore	$0.20 < \varphi \leq 0.30$	4	$0.30 < \varphi \leq 0.50$	1	$0.50 < \varphi$	None
Diameter	Acc. Qty											
$\varphi \leq 0.20$	Ignore											
$0.20 < \varphi \leq 0.30$	4											
$0.30 < \varphi \leq 0.50$	1											
$0.50 < \varphi$	None											
13	Bezel	13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.										
14	Touch Panel	D: Diameter W: width L: length 14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$ 2dots are acceptable and the distance between defects should more than 10 mm. $D > 0.4$ is unacceptable 14.2 Dent: $D > 0.40$ is unacceptable 14.3 Scratch: $W \leq 0.03$, $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$, $L \leq 10$ is acceptable Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.										
15	LCD Ripple	Touch the touch panel, cannot see the LCD ripple. Pen: R 0.8mm silicon rubber. Operation Force:120g										
16	PCB	16.1 No distortion or contamination on PCB terminals. 16.2 All components on PCB must same as documented on the BOM/component layout. 16.3 Follow IPC-A-600F.										

17	Soldering	Follow IPC-A-610C standard
18	Electrical Defect (Major defect)	The below defects must be rejected. 18.1 Missing vertical / horizontal segment, 18.2 Abnormal Display. 18.3 No function or no display. 18.4 Current exceeds product specifications. 18.5 LCD viewing angle defect. 18.6 No Backlight. 18.7 Dark Backlight. 18.8 Touch Panel no function.

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

12.7. Classification of Defects

12.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

12.7.2. Two minor defects are equal to one major in lot sampling inspection.

12.8. Identification/marketing criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

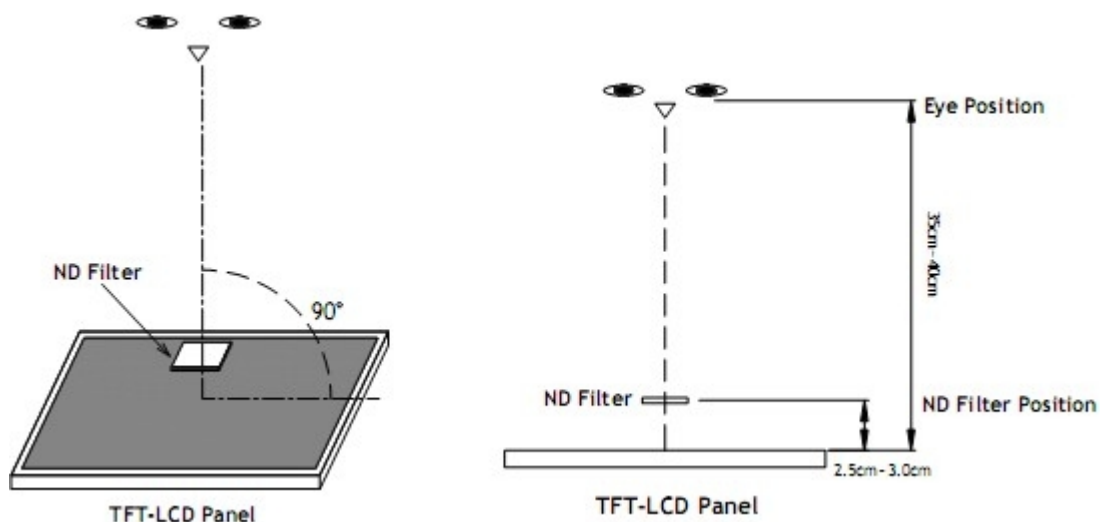
12.9. Packing

12.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

12.9.2. Modules inside package box should have compliant mark.

12.9.3. All direct package materials shall offer ESD protection.

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is 350mm $\pm$ 50mm.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is 350mm $\pm$ 50mm.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

13. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	+80°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	-30°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity Storage	+50°C, 90%RH, 96Hrs	2	GB/T2423.3-2016
4	High Temperature Storage	+80°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test Storage	-20°C, 60min~+70°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	-	GB/T5170.14-2009
8	Electrical Static Discharge	Air: $\pm 4\text{kV}$ 150pF/330 Ω 5 times Contact: $\pm 2\text{kV}$ 150pF/330 Ω 5 times	2	GB/T17626.2-2018
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	-	GB/T2423.7-2018

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value.

14. Precautions and Warranty

14.1. Safety

- 14.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 14.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

14.2. Handling

- 14.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 14.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

14.3. Storage

- 14.3.1. Do not store the LCD module beyond the specified temperature ranges.

14.4. Metal Pin (Apply to Products with Metal Pins)

14.4.1. Pins of LCD and Backlight

- 14.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

14.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

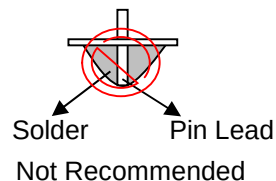
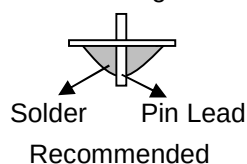
Maximum Solder Temperature: 370°C

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20°C

Typical Soldering Time: ≤3s

14.4.1.3. Solder Wetting



14.4.2. Pins of EL

- 14.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

- 14.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

14.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

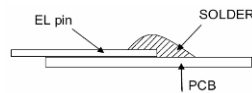
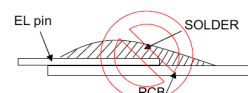
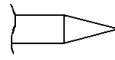
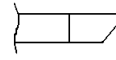
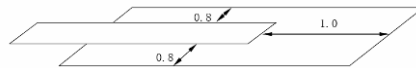
Recommended Solder Temperature: 270~290°C

Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body): 2.0mm

- 14.4.2.4. No horizontal press on the EL leads during soldering.

- 14.4.2.5. 180° bend EL leads three times is not allowed.

14.4.2.6. Solder Wetting**Recommended****Not Recommended****14.4.2.7. The type of the solder iron:****Recommended****Not Recommended****14.4.2.8. Solder Pad****14.5. Operation**

- 14.5.1. Do not drive LCD with DC voltage
- 14.5.2. Response time will increase below lower temperature
- 14.5.3. Display may change color with different temperature
- 14.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear "fractured".

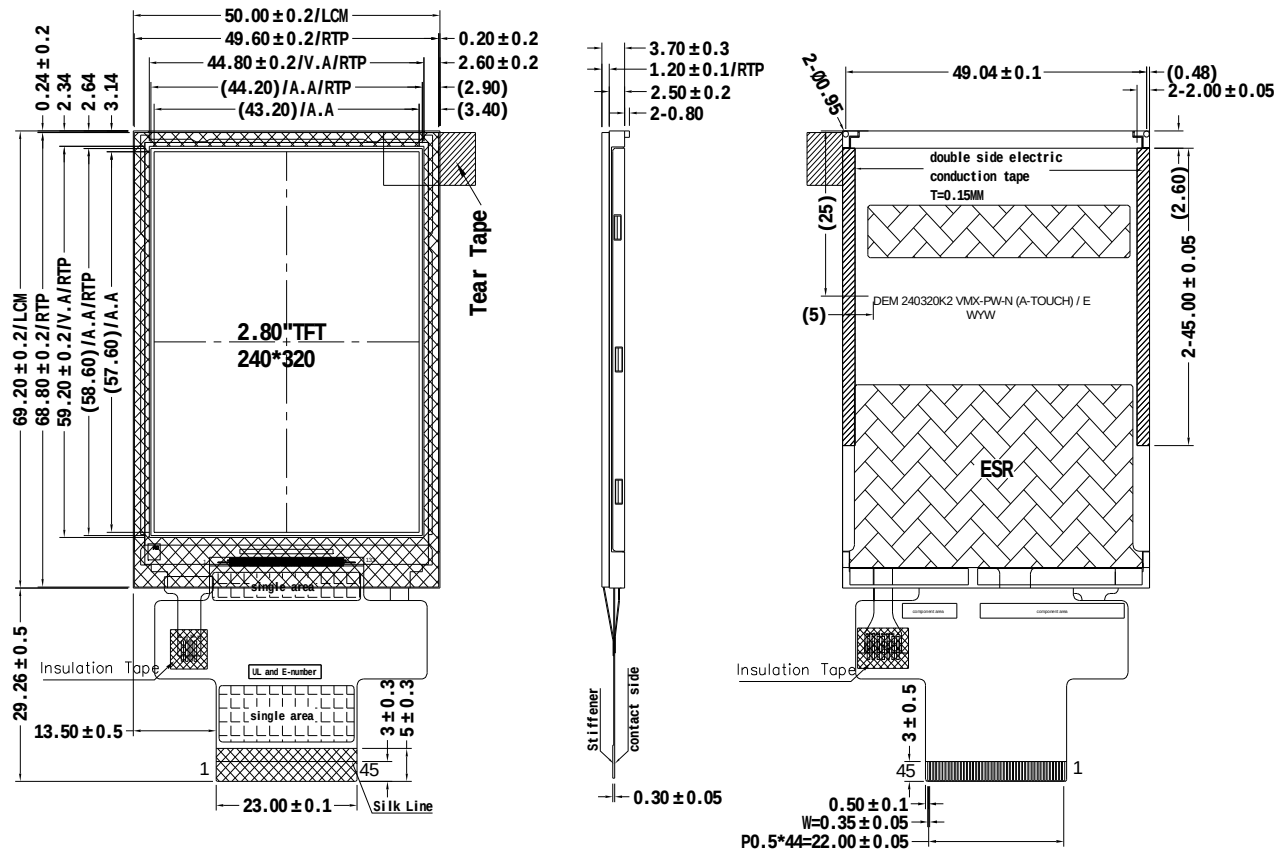
14.6. Static Electricity

- 14.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 14.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 14.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

14.7. Limited Warranty

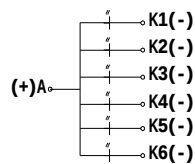
- 14.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 14.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 14.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

15. Outline Drawing



NOTES:

1. Display size: $2.8''$ TFT IPS
 2. Viewing direction: full view
 3. Display mode: Transmissive/Normal Black/Anti-glare
 4. Operation temperature: -30°C to $+80^{\circ}\text{C}$
 5. Storage temperature: -30°C to $+80^{\circ}\text{C}$
 6. Driver IC: ST7789T3
 7. Power supply voltage: 2.8 V
 8. Backlight : White(6LED)/ 3.2 (TYP)V/ 120mA
 9. Luminance: 550 (TYP) cd/m^2 (With RTP)
 10. Interface type: IM0-IM3 Pull out the optional MCU, SPI, RGB
 11. RoHS must be complied
- * The dimension with mark brackets "()" just for reference



Backlight LED Circuit: $6 \times 1 = 6$ (LED)
 $I_f = 120\text{mA}$; $V_f = 2.9\text{--}3.3\text{V}$
 (LED Lifetime: 50000h)

LCM

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	EXTC	24	DB15
2	IOVCC(1.8/2.8V)	25	DB16
3	VCC(2.8V)	26	DB17
4	/CS	27	SDA
5	D/C(SCL)	28	SDO
6	/WR	29	TE
7	/RD	30	DOTCLK
8	/RESET	31	VSYNC
9	DB0	32	HSYNC
10	DB1	33	ENABLE
11	DB2	34	IM0
12	DB3	35	IM1
13	DB4	36	IM2
14	DB5	37	IM3
15	DB6	38	YD
16	DB7	39	XR
17	DB8	40	YU
18	DB9	41	XL
19	DB10	42	LED_A
20	DB11	43	LED_K
21	DB12	44	GND
22	DB13	45	GND
23	DB14		